FACULTY OF SCIENCE



Course Scheme & Syllabus
For
Bachelor of Computer Applications
(Hons.)

(As per NEP-2020) Batch-2024 & onwards

Introductory Note of the Programme

The BCA program is designed to equip you with the knowledge and skills necessary to thrive in the rapidly evolving field of information technology. Over the course of this program, you will explore various aspects of computer science, including programming languages, database management, software development, networking, web development, and much more. Our curriculum is carefully crafted to strike a balance between theoretical knowledge and practical application, ensuring that you not only grasp the fundamental concepts but also gain hands-on experience in solving real-world problems.

Upon completion of the BCA program, you will be well-prepared to pursue a wide range of career opportunities in the IT industry. Whether you aspire to become a software developer, systems analyst, database administrator, web developer, or pursue higher studies, the BCA program will lay a solid foundation for your future endeavors.

Program Educational Objectives (PEOs)

- **PEO-1.** Work productively as successful Computer professionals in diverse career paths including supportive and leadership roles on multidisciplinary teams or be active in higher studies.
- **PEO-2.** Communicate effectively, recognize and incorporate societal needs and constraints in their professional endeavors, and practice their profession with high regard to ethical responsibilities.
- **PEO-3.** Engage in life-long learning and to remain current in their profession to foster personal and organizational growth.

Programme Outcomes (POs)

- **PO-1:** Apply mathematics and computing fundamental and domain concepts to find out the solution of defined problems and requirements. (Computational Knowledge)
- **PO-2:** Use fundamental principle of Mathematics and Computing to identify, formulate research literature for solving complex problems, reaching appropriate solutions. (Problem Analysis)
- **PO-3:** Understand to design, analyze and develop solutions and evaluate system components or processes to meet specific need for local, regional and global public health, societal, cultural, and environmental systems. (Design/Development of Solutions)
- **PO-4:** Use expertise research-based knowledge and methods including skills for analysis and development of information to reach valid conclusions. (Conduct Investigations of Complex Computing Problems)
- **PO-5:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations. (Modern tool usage)

PO-6: Exhibiting ethics for regulations, responsibilities and norms in professional computing practices. (Professional Ethics)

PO-7: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development (Environment and sustainability).

PO-8: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice (Ethics).

PO-9: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings (Individual and team work).

PO-10: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions (Communication).

PO-11: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments (Project management and finance).

PO-12: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change (Life-long learning).

Program Specific Objectives (PSOs)

PSO-1: To explore technical comprehension in varied areas of Computer Applications and experience a conducive environment in cultivating skills for thriving career and higher studies.

PSO-2: To comprehend, explore and build up computer programs in the allied areas like Algorithms, System Software, Multimedia, Web Design and Data Analytics for efficient design of computer-based systems of varying complexity.

Mapping of POs with PEOs

| PEOs→ | PEO 1 | PEO 2 | PEO 3 |
|-------|-------|-------|-------|
| ₽Os↓ | | | |
| P01 | Yes | | Yes |
| PO2 | | | Yes |
| PO3 | Yes | | Yes |
| PO4 | | Yes | |
| PO5 | Yes | Yes | |
| PO6 | | | Yes |
| PO7 | Yes | | Yes |
| PO8 | | | |
| PO9 | | Yes | |
| PO10 | Yes | | Yes |
| PO11 | | Yes | |
| PO12 | Yes | Yes | |

Mapping of PSO with PEO

| PEOs→ | PEO 1 | PEO 2 | PEO 3 |
|-------|-------|-------|-------|
| PSO↓ | | | |
| PSO1 | Yes | | Yes |
| PSO2 | | Yes | Yes |

Scheme of Courses Bachelor of Computer Applications

| | Credit Details | | |
|-------|--|------------------------------------|--------------------------|
| S.No. | Course Category | Course Category Abbreviation | 3-Yr B.C.A/ (Credits) |
| 1.1 | Discipline Specific Courses-Core | DSC | 52 |
| 1.2 | Discipline Specific-Skill Enhancement Courses- Core | DS-SEC | 09 |
| 1.3 | Discipline Specific-Value Added Courses-Core | DS-VAC | |
| | Total of Discipline Specific Core Course | S | |
| 2.1 | Minor Courses | MC | 20 |
| | OR | | |
| 2.2 | Interdisciplinary Courses | IDC | 04 |
| 3 | Multidisciplinary Courses | MDC | 09 |
| 4 | Ability Enhancement Course- Common | AEC-C | 08 |
| 5 | Value Added Courses-Common | VAC-C | 06 |
| 6.1 | Skill Enhancement Courses- Common | SEC-C | 08 |
| 6.2 | Skill Enhancement Courses-Summer Internship | SEC-SI | 04 |
| | Total of Skill Enhancement Courses | | |
| | Total Credits | | 120 |

Scheme of Courses Bachelor of Computer Applications

| | Cro | edit Details | | |
|-------|---|------------------------------------|--------------------------------------|--|
| S.No. | Course Category | Course Category Abbreviation | 4-Yr B.C.A. (Hons.)/ (Credits) | 4-Yr B.C.A. (Hons/ (Hons. with Res.) (Credits) |
| 1.1 | Discipline Specific Courses-Core | DSC | 84 | 72 |
| 1.2 | Discipline Specific-Skill Enhancement Courses-Core | DS-SEC | 09 | 09 |
| 1.3 | Discipline Specific-Value Added Courses-Core | DS-VAC | | |
| | Total of Discipline Specific C | ore Courses | | |
| 2.1 | Minor Courses | MC | 28 | 28 |
| | | OR | | |
| 2.2 | Interdisciplinary Courses | IDC | 04 | 04 |
| 3 | Multidisciplinary Courses | MDC | 09 | 09 |
| 4 | Ability Enhancement Course- Common | AEC-C | 08 | 08 |
| 5 | Value Added Courses-Common | VAC-C | 06 | 06 |
| 6.1 | Skill Enhancement Courses- Common | SEC-C | 08 | 08 |
| 6.2 | Skill Enhancement Courses- Summer Internship | SEC-SI | 04 | 04 |
| 6.3 | Skill Enhancement Courses- Research Project/Dissertation | SEC-RP | | 12 |
| | | ourses | | |
| | Course Category | | | 160 |

Semester 1

| S. No | Paper | Course Title | Course Category | L | T | P | Cr |
|-------|--------|---|------------------------|---|---|---|----|
| | Code | | | | | | |
| 1 | CSP101 | Principles of Digital Electronics | DSC | 3 | 0 | 0 | 3 |
| 2 | CSP102 | Computer Fundamentals and Office Automation | DS-SEC | 2 | 0 | 2 | 3 |
| 3 | CSP103 | Algorithm Design and Programming Using C | DSC | 3 | 0 | 2 | 4 |
| 4 | XXXX | Multi-Disciplinary Course | MDC | 3 | 0 | 0 | 3 |
| 5 | XXXX | Ability-Enhancement Course | AEC-C | 2 | 0 | 0 | 2 |
| 6 | XXXX | Skill-Enhancement Course (common) | SEC-C | 2 | 0 | 0 | 2 |
| 7 | XXXX | Value-added Course | VAC-C | 2 | 0 | 0 | 2 |
| | | To | tal | • | • | | 19 |

L-Lectures T-Tutorial P-Practical Cr.- Credits

| S. No | Paper | Course Title | Course Category | L | T | P | Cr |
|-------|--------|---|-----------------|---|---|---|----|
| | Code | | | | | | |
| 1 | CSP104 | Object Oriented Programming using C++ | DSC | 3 | 0 | 2 | 4 |
| 2 | CSP105 | Web Designing | DS-SEC | 1 | 0 | 2 | 2 |
| 3 | CSP106 | Mathematical Foundation of Computer Science | DSC | 3 | 0 | 0 | 3 |
| 4 | XXXX | Multi -Disciplinary Course | MDC | 3 | 0 | 0 | 3 |
| 5 | XXXX | Ability-Enhancement Course | AEC-C | 2 | 0 | 0 | 2 |
| 6 | XXXX | Skill-Enhancement Course (common) | SEC-C | 3 | 0 | 0 | 3 |
| 7 | XXXX | Value-added Course | VAC-C | 1 | 0 | 2 | 2 |
| 8 | XXXX | Value-added Course | VAC-C | 1 | 1 | 0 | 2 |
| | | To | tal | • | • | • | 21 |

L- Lectures T- Tutorial P- Practical Cr.- Credits

FIRST EXIT:

The students will be awarded "Undergraduate Certificate in Computer Science & Applications" after exit at this point, provided they secure 4 Credits in skill/work-based vocational courses or internship/apprenticeship for 4-6 weeks (with minimum 120 hours) during summer term.

Semester 3

| S. | Paper Code | Course Title | Course Category | L | T | P | Cr |
|----|------------|-----------------------------------|------------------------|---|---|---|----|
| No | | | | | | | |
| 1 | CSP201 | Computer Oriented | | | | | |
| | | Numerical and Statistical | IDC | 4 | 0 | 0 | 4 |
| | | Techniques | | | | | |
| 2 | CSP202 | Object Oriented | DSC | 3 | 0 | 2 | 4 |
| | | Programming using Java | DSC | 3 | U | | 4 |
| 3 | CSP203 | Database Concepts | DSC | 3 | 0 | 2 | 4 |
| 4 | XXXX | Multi -Disciplinary Course | MDC | 3 | 0 | 0 | 3 |
| 5 | XXXXX | Ability-Enhancement Course | AEC-C | 2 | 0 | 0 | 2 |
| 6 | XXXX | Skill-Enhancement Course (common) | SEC-C | 3 | 0 | 0 | 3 |
| | | Tota | al | • | | | 20 |

L-Lectures T-Tutorial P-Practical Cr.- Credits

| S. | Paper | Course Title | Course Category | L | T | P | Cr |
|-----|-------------|---|--------------------------|-----|---|---|----|
| No | Code | | | | | | |
| 1 | CSP204 | Data Structures | DSC | 3 | 0 | 2 | 4 |
| 2 | CSP205 | Computer Graphics | MC | 3 | 0 | 2 | 4 |
| 3 | CSP206 | Operating Systems | DSC | 3 | 0 | 0 | 3 |
| 4 | CSP207 | Computer Organization and Architecture | DSC | 3 | 0 | 0 | 3 |
| 5 | CSP208 | Computer Networks | DSC | 3 | 0 | 2 | 4 |
| 6 | XXXX | Ability-Enhancement Course | AEC-C | 2 | 0 | 0 | 2 |
| | | | Total | | | | 20 |
| NCC | credits are | e only earned by those | students who are opted N | NCC | | | |
| 7 | NCC201 | NCC Organization and National Integration | VAC | 2 | 0 | 0 | 2 |
| 8 | NCC202 | Training : Drill, Map Reading, Field And Battle Craft | VAC | 0 | 0 | 2 | 1 |
| | | | Total | | | | 23 |

L- Lectures T- Tutorial P- Practical Cr.- Credits

SECOND EXIT:

The student will be awarded "Undergraduate Diploma in Computer Science & Applications" after exit at this point provided that he/she secure 4 Credits in skill/work based vocational courses or internship/apprenticeship for 4-6 weeks (with minimum 120 hours) offered during first year summer term or second year summer term.

Semester 5

| S.No | Paper Code | Course Title | Course Category | L | Т | P | Cr |
|------|---------------|---|------------------------|--------|----|---|----|
| 1 | CSP301 | Internship | SEC-SI | 0 | 0 | 8 | 4 |
| 2 | CSP302 | Programming in Python | DSC | 3 | 0 | 2 | 4 |
| 3 | | | DS-SEC | 3 | 0 | 2 | 4 |
| 4 | CSP303 | Web Engineering using ASP.NET | MC | 3 | 0 | 2 | 4 |
| 5 | CSP304 | Cyber Security | MC | 4 | 0 | 0 | 4 |
| | | | Total | | | | 20 |
| NCC | credits are | only earned by the | ose students who are o | pted N | CC | | |
| 7 | NCC203 | NCC Organization and National Integration | VAC | 2 | 0 | 0 | 2 |
| 8 | NCC204 | Training : Drill, Map Reading, Field And Battle Craft | VAC | 0 | 0 | 2 | 1 |
| | | | Total | | | | 23 |

L- Lectures T- Tutorial P- Practical Cr.- Credits

DS-SEC (Discipline Specific-Skill Enhancement Course-Core)- (Choose One)

| S.No | Paper Code | Course Title | L | T | P | Cr |
|------|------------|-----------------------------|---|---|---|----|
| 1 | CSP307 | Data Warehousing and Mining | 3 | 0 | 2 | 4 |
| 2 | CSP308 | Data Analytics | 3 | 0 | 2 | 4 |
| 3 | CSP309 | Big Data | 3 | 0 | 2 | 4 |

Semester 6

| S.No | Paper | Course Title | Course Category | L | T | P | Cr |
|------|--------|----------------------------------|-----------------|---|---|---|----|
| | Code | | | | | | |
| 1 | CSP310 | Design and Analysis of Algorithm | DSC | 3 | 0 | 0 | 3 |
| 2 | CSP311 | Artificial Intelligence | DSC | 3 | 0 | 0 | 3 |
| 3 | CSP312 | Software Engineering | DSC | 3 | 0 | 0 | 3 |
| 4 | CSP313 | Mobile Application Development | DSC | 3 | 0 | 2 | 4 |
| 5 | CSP314 | Discrete Mathematics | DSC | 3 | 0 | 0 | 3 |
| 6 | | | MC | 4 | 0 | 0 | 4 |
| | | | Total | | | | 20 |

MC (MINOR COURSE)-(Choose One)

| S.No | Paper Code | Course Title | L | T | P | Cr |
|------|------------|--------------------------|---|---|---|----|
| 1 | CSP315 | Digital Image Processing | 3 | 0 | 2 | 4 |
| 2 | CSP316 | R Programming | 3 | 0 | 2 | 4 |
| 3 | CSP317 | Machine Learning | 3 | 0 | 2 | 4 |

L- Lectures T- Tutorial P- Practical Cr.- Credits

Note: If the Student get CGPA >= 7.5 then He/ She will have to submit the Research Project with 12 Credit.

THIRD EXIT:

The student will be awarded "Bachelors in Computer Science & Applications" degree after exit at this point.

Semester 7

| S.No | Paper | Course Title | Course Category | L | T | P | Cr |
|------|--------|------------------------------|-----------------|---|---|---|----|
| | Code | | | | | | |
| 1 | CSP405 | Theory of Computer Science | DSC | 4 | 0 | 0 | 4 |
| 2 | CSP404 | Advanced in Operating System | DSC | 3 | 0 | 2 | 4 |
| 3 | | | DS-SEC | 4 | 0 | 0 | 4 |
| 4 | CSP401 | Research Methodology | MC | 4 | 0 | 0 | 4 |
| 5 | CSP409 | Mobile Computing | DSE | 4 | 0 | 0 | 4 |
| | | | Total | | | | 20 |

DS-SEC (Discipline Specific-Skill Enhancement Course-Core)- (Choose One)

| S.No | Paper Code | Course Title | | T | P | Cr |
|------|------------|--------------------|---|---|---|----|
| 1 | CSP406 | Compiler Design | 3 | 0 | 0 | 3 |
| 2 | CSP407 | System Programming | 3 | 0 | 0 | 3 |

Semester 8

| S.No | Paper | Course Title | Course Category | L | T | P | Cr |
|------|---------|---|------------------------|---|---|----|----|
| | Code | | | | | | |
| 1 | CSP402 | Internet of Things | DSC | 3 | 0 | 2 | 4 |
| 2 | CSP411 | Digital Image Processing | DSE | 3 | 0 | 2 | 4 |
| 3 | CSP410* | Major Project | | 0 | 0 | 12 | 6 |
| 4 | CSP412* | Cryptography and Network Security | MC | 2 | 0 | 2 | 3 |
| 5 | CSP420 | Cloud Computing Security & Management | DSC | 4 | 0 | 0 | 4 |
| | | | Total | | | | 21 |

• *Those students are adopting the research project they are exempted these courses. (12 credit)

| S.No | Paper Code | Course Title | Course Category | L | T | P | Cr |
|------|---------------|------------------|-----------------|---|---|----|----|
| 1 | CSP415 | Research Project | | 0 | 0 | 24 | 12 |
| | | | Total | | | | 12 |

FOURTH EXIT:

The student will be awarded "Bachelor of Computer Science and Applications (Hons.)" degree after exit at this point.

List of multi-disciplinary open elective courses at DAV University

| Sr. No. | Course Name | Faculty/Department |
|---------|-----------------------------------|------------------------------|
| 1 | Basics of Physics | Physics |
| 2 | Basics of Chemistry | Chemistry |
| 3 | Basics of Biology | Zoology & Botany |
| 4 | Introductory Biotechnology | Biotechnology |
| 5 | Introductory Microbiology | Microbiology |
| 6 | Functioning of the Human Body | Zoology |
| 7 | Introductory Botany | Botany |
| 8 | Business Management for Beginners | СВМЕ |
| 9 | Fundamental of Mutual Funds | СВМЕ |
| 10 | Economics for Beginners | СВМЕ |
| 11 | Professional Communication | English |
| 12 | Fine Arts | Arts, Fine Arts & Performing |
| | | Arts |
| 13 | Jyotish: 'Eye of the Veda' | Vedic Studies |
| 14 | Mathematical Statistics | Mathematics |
| 15 | Introductory Journalism | JMC |
| 16 | Professional Photography | JMC |
| 17 | Library Information Sciences | Library Sciences |

Common courses with credits

| Ability- Enhancement Courses | Cr. | Skill- Enhancement Courses | Cr. | Value-Added Courses | Cr. |
|---|-------|---|-------|---|-------|
| Personality Enhancement | 1L+1P | Essentials of Entrepreneurship- Thinking and Action | 2L+1P | Environmental Studies (Mandatory) | 2L+1P |
| Personality Development | 2P | Design Thinking | 2P | Human Values and Ethics (Mandatory) | 2L+1T |
| Behavioural & Life Skills | 1L+1P | Design Thinking & Innovation | 2L | Gender Sensitization | 2L |
| Global Citizenship in Higher Education | 2L | Data Analytics | 2L+1P | Professional Ethics | 2L |

| Communication Skills (Mandatory) | 1L+1P | Cyber Security | 3 (2L+1P) | Sustainable Development | 2L |
|--|-------|---|--------------|----------------------------|-----------|
| OR Cambridge English-I | | Digital Fluency | 1L+1P | Green Technologies | 2L |
| (Mandatory#) & | 1L+1P | | | | |
| Cambridge English-II (Mandatory#) | 1L+1P | | | | |
| # To be offered in two semesters | | | | | |
| Health & Yoga | 1L+1P | Fundamentals of Computer programming & IT(FCPIT) | 2L | General Studies | 2L |
| Technical Report Writing | 2L | Python Programming | 3 (2L+1P) | NSS | 2 (1L+1P) |
| Leadership Management | 2L | Disaster Preparedness and Planning | 2L | | |
| Therapeutic Yoga | 1L+1P | Intellectual Property Rights | 2L | | |
| Creative & Critical Thinking | 1L+1P | Apiculture | 2P | | |
| Community Engagement & Social Responsibility (Mandatory) | 1L+1P | NCC* | 3 (2L+1P) | | |
| | | LATEX | 3 (1L+2P) | | |
| | | Programming with FORTRAN | 3(2L+1P) | | |

Notes:

- a. Due to the constraint on total number of credits to be restricted under 160 for four year UG programmes, the mandatory courses which may or may not fall under ability-enhancement, skill-enhancement (common) or value- added courses can be offered as non-credit course and the student will have to qualify (as Satisfactory/Unsatisfactory) these courses to secure minimum passing marks through the process of assessment as mandated by DAV University.
- b.Minimum number of students feasible to run a common course (Ability- enhancement, Skill-enhancement (common) and Value-added) will be 20 students.
- c. *Pre-requisite to opt NCC is that the student must be in possession of Certificate B or has appeared in B-certificate exam of NCC. NCC course shall run in two semesters of 3 credits (2L+1P) in each semester. Student who wishes to opt for NCC is required to study in two semesters of total 6 credit



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 0 | 3 |

| Course Title Course On the completion of the course the student will be able to CO1: To provide the knowledge about the various electronics components and digital circuits to the students and designing of various building blocks of computer system concepts. CO2: To introduce the basic concepts and laws involved in the Boolean algebra and logic families and digital circuits. CO3: To familiarize with the different number systems, logic gates, and combinational and sequential circuits utilized in the different digital circuit and systems. CO4: To design and analysis of the digital circuit and systems. After studying these subject students will be able to easily understand the internal working of digital electronic circuits. Examination Mode Assessment Proof Practical/Theory + Practical Written Proof Practical/Theory + Practical Mode Assessment Proof Practical/Theory + Practical Mode Assessment Proof Practical/Theory + Practical Tools Written Proof Practical/Theory + Practical Tools Tool | Course Code | CSP101 | CSP101 | | | | | | | | |
|--|--------------|---------------|--|-----------------|----------------|------------------|---|---------------|--|--|--|
| Outcomes CO1: To provide the knowledge about the various electronics components and digital circuits to the students and designing of various building blocks of computer system concepts. CO2: To introduce the basic concepts and laws involved in the Boolean algebra and logic families and digital circuits. CO3: To familiarize with the different number systems, logic gates, and combinational and sequential circuits utilized in the different digital circuits and systems. CO4: To design and analysis of the digital circuit and system. After studying these subject students will be able to easily understand the internal working of digital electronic circuits. Examination Mode Assessment Practical/ Theory + Practical Written Assignment/ MSE MTP ESE EPR ABL/PBL Quiz Project Work Quiz Project Work Weightage 10% 10% 25% - 50% - 5% CO1 **Operations of Digital Electronics & Number System CO1 Definitions of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO2 Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits CO3 Multiplexers & De-multiplexers Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | Course Title | Principle | | | | | | | | | |
| the students and designing of various building blocks of computer system concepts. CO2: To introduce the basic concepts and laws involved in the Boolean algebra and logic families and digital circuits. CO3: To familiarize with the different number systems, logic gates, and combinational and sequential circuits utilized in the different digital circuit and systems. CO4: To design and analysis of the digital circuit and systems. After studying these subject students will be able to easily understand the internal working of digital electronic circuits. Examination Mode Assessment Theory/ Practical/ Theory + Practical Written Assignment/ MSE MTP ESE EPR ABL/PBL Quiz Project Work 25% - 50% - 5% Syllabus Unit 1 Fundamentals of Digital Electronics & Number System CO 1 Fundamentals of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Gonversion from One Number System to another, Arithmetic Operation without Changing the Base, 1''s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO 2 Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits CO 3 Multiplexers & Deconders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | Course | On the co | On the completion of the course the student will be able to | | | | | | | | |
| CO2: To introduce the basic concepts and laws involved in the Boolean algebra and logic families and digital circuits. CO3: To familiarize with the different number systems, logic gates, and combinational and sequential circuits utilized in the different digital circuits and systems. CO4: To design and analysis of the digital circuit and system. After studying these subject students will be able to easily understand the internal working of digital electronic circuits. Examination Mode Assessment Theory Practical/ Theory + Practical Written Assignment/ MSE MTP ESE EPR ABL/PBL Tools Quiz Project Work Weightage 10% 10% 25% - 50% - 5% Syllabus Unit 1 Fundamentals of Digital Electronics & Number System CO Mapping Unit 1 Fundamentals of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Gonversion from One Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base,1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Maps. Half Adder& Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | Outcomes | CO1: To p | · · · · · · · · · · · · · · · · · · · | | | | | | | | |
| families and digital circuits. CO3: To familiarize with the different number systems, logic gates, and combinational and sequential circuits utilized in the different digital circuits and systems. CO4: To design and analysis of the digital circuit and system. After studying these subject students will be able to easily understand the internal working of digital electronic circuits. Examination Mode Assessment Mode Assessment Tools Quiz Project Work MSE MTP ESE EPR ABL/PBL Quiz Project Work Weightage 10% 10% 25% - 50% - 5% CO Mapping Unit 1 Fundamentals of Digital Electronics & Number System CO 1 Definitions of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits Fundamentals of Digital Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Subtractor, Englementation of Boolean equations using Multiplexer and Demultiplexer and Demultiplexer Encoders & Decoders. Examination Logic Sates, K Maps, Simplification of Boolean equations using Multiplexer and Demultiplexer and Demultiplexer and Demultiplexer and Demultipl | | the studen | the students and designing of various building blocks of computer system concepts. | | | | | | | | |
| CO3: To familiarize with the different number systems, logic gates, and combinational and sequential circuits untilized in the different digital circuits and systems. CO4: To design and analysis of the digital circuit and system. After studying these subject students will be able to easily understand the internal working of digital electronic circuits. Examination Mode Theory/ Practical/ Theory + Practical Mode Written Assignment/ MSE MTP ESE EPR ABL/PBL Tools Quiz Project Work Weightage 10% 10% 25% - 50% - 5% Syllabus Unit 1 Fundamentals of Digital Electronics & Number System CO Mapping Unit 1 Fundamentals of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits Boolean Algebra & Combinational Circuits Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits CO3 Hultiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. | | CO2: To | | | | | | | | | |
| Sequential circuits utilized in the different digital circuits and systems. CO4: To design and analysis of the digital circuit and system. After studying these subject students will be able to easily understand the internal working of digital electronic circuits. Examination Mode | | families ar | | | | | | | | | |
| CÓ4: To design and analysis of the digital circuit and system. After studying these subject students will be able to easily understand the internal working of digital electronic circuits. Theory/ Practical/ Theory + Practical Theory + Practical/ Theory + Practical Assessment Mode Assessment Tools Weightage 10% 10% 25% - 50% - 5% Syllabus Tendamentals of Digital Electronics & Number System Operations of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits Logic Gates, K Maps, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder& Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits CO 3 Mapping CO 1 CO 1 CO 2 Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | CO3: To | familiarize with the o | different numb | er systems, | logic gates, | and comb | inational and | | | |
| Students will be able to easily understand the internal working of digital electronic circuits. | | sequential | circuits utilized in the | different digit | al circuits ar | nd systems. | | | | | |
| Examination Mode Assessment Written Assignment/ MSE MTP ESE EPR ABL/PBL Tools Quiz Project Work Weightage 10% 10% 25% - 50% - 5% Syllabus Unit 1 Fundamentals of Digital Electronics & Number System Definitions of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | CO4: To | design and analysis of | of the digital | circuit and | system. After | studying | these subject | | | |
| Mode | | | | | ternal work | ing of digital o | electronic | circuits. | | | |
| Assessment Tools Quiz Project Work Project Proje | Examination | Theory/ F | Practical/ Theory + P | ractical | | | | | | | |
| Tools Quiz Project Work Weightage 10% 10% 25% - 50% - 55% CO Mapping Unit 1 Fundamentals of Digital Electronics & Number System CO 1 • Definitions of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. • Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. • Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO 2 • Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. • Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits CO 3 • Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer • Encoders & Decoders. • Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | Mode | | T | | | | | | | | |
| Weightage 10% 10% 25% - 50% - 5% CO Mapping | Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | | |
| Unit 1 Fundamentals of Digital Electronics & Number System OCO 1 Definitions of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base,1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO 2 Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Nultiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | Tools | Quiz | Project Work | | | | | | | | |
| Unit 1 Fundamentals of Digital Electronics & Number System CO 1 Definitions of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO 2 Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder& Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | Weightage | 10% | 10% | 25% | - | 50% | - | 5% | | | |
| Unit 1 Fundamentals of Digital Electronics & Number System Definitions of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder& Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | Syllabus | | | | | | | CO | | | |
| Definitions of Digital Signals, Digital Waveform, Digital Logic, Gate propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base,1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using K Maps. Half Adder& Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | | | | | | | Mapping | | | |
| propagation delay time, Digital Operations, Digital Integrated Circuits, Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base,1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO 2 Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder& Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits CO 3 Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | Unit 1 | Fundame | entals of Digital Ele | ctronics & N | lumber Sy | stem | | CO 1 | | | |
| Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO 2 Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | • | Definition | ns of Digital Signa | ls, Digital V | Vaveform, | Digital Log | ic, Gate | | | | |
| Digital IC signal levels. Decimal Number System, Binary Number System, Octal Number System, Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO 2 Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | propagati | on delay time, Dig | ital Operatio | ns, Digital | Integrated | Circuits, | | | | |
| Hexadecimal Number System, Conversion from One Number System to another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO 2 Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | | | • | | C | | | | | |
| another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO 2 Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using K Maps. Half Adder& Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | • | Decimal | Number System, Bir | nary Number | System, O | ctal Number | System, | | | | |
| another, Arithmetic Operation without Changing the Base, 1"s Complement and 2's Complement. Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits CO 2 Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using K Maps. Half Adder& Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | Hexadeci | mal Number Systen | n, Conversion | n from One | e Number Sy | ystem to | | | | |
| Logic Gates: AND, OR, NOT, NAND, NOR, XOR, XNOR, NAND & NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | | • | | | - | | | | | |
| NOR as Universal Gates, Logic Gates Applications. Unit 2 Boolean Algebra & Combinational Circuits Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | and 2's C | omplement. | | | | - | | | | |
| Unit 2 Boolean Algebra & Combinational Circuits Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | • | Logic Ga | ites: AND, OR, NO | T, NAND, I | NOR, XOR | R, XNOR, N | AND & | | | | |
| Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | NOR as U | Jniversal Gates, Log | ic Gates App | lications. | | | | | | |
| Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | | | | | | | | | | |
| Introduction, Theorems, Simplification of Boolean Expression using Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | Unit 2 | Boolean | Algebra & Combin | ational Circi | uits | | | CO 2 | | | |
| Boolean Algebra, SOP & POS Forms, Realization of Boolean Expression using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits CO 3 Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | • | | | | | n Expressio | n using | | | | |
| using Gates, K Maps, Simplification of Boolean Expression using K Maps. Half Adder& Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | | | | | | | | | | |
| Half Adder & Half Subtractor, Full Adder & Full Subtractor, Parallel Binary Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | | _ | | | | - | | | | |
| Adder, Binary Adder/ Subtractor. Unit 3 Combinational & Sequential Logic Circuits Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | • | | | | | | | | | | |
| Unit 3 Combinational & Sequential Logic Circuits CO 3 Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | | | | | | 2 | | | | |
| Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | 110001, 21 | and J 114441, Suetau | | | | | | | | |
| Multiplexers & De-multiplexers, Implementation of Boolean equations using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | Unit 3 | Combina | Combinational & Sequential Logic Circuits CO 3 | | | | | | | | |
| using Multiplexer and Demultiplexer Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | | | | | oolean equat | tions | | | | |
| Encoders & Decoders. Latch, Flip Flops RS Flip Flop, JK Flip Flop, Master Slave JK Flip Flop | | | | | | 1 | | | | | |
| | • | | | | | | | | | | |
| | • | Latch, Fli | p Flops RS Flip Flor | o, JK Flip Flo | p, Master S | Slave JK Flir | Flop | | | | |
| | | | | • • | • | | - | | | | |

| | Applications of Flip Flops | |
|---------------------|---|------|
| Unit 4 | Semiconductor & Memories | CO 4 |
| • | Introduction, Static and dynamic devices, read only & random-access memory chips, PROMS and EPROMS Address selection logic. | |
| • | Read and write control timing diagrams for ICs. | |
| D - f | 1. Malaina Diaital Communica Electronica Dalki McCorre Hill Consul | |
| Reference Book/s | Malvino, Digital Computer Electronics, Delhi: McGraw Hill, Second Edition. ManoD. Morris, Digital Logic & Computer Design, New Delhi: PHI Second Edition | |
| | Halkias Millman, Integrated Electronics, Delhi: McGraw Hill. Hodges D.A. & Jackson H.G., Analysis and Design of Integrated Circuits, New York: International McGraw Hill, 1983. Ujjen beck, John, Digital Electronics: A Modern Approach, | |
| | New Delhi: Prentice Hall, 1994 | |



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 2 | 4 |

| Course Code | CSP102 | CSP102 | | | | | | | | |
|--------------|--|--|-----------------|-------------------|-----------------|------------|-------------|--|--|--|
| Course Title | | er Fundamentals ar | nd Office Au | tomation | | | | | | |
| Course | | ompletion of the cour | | | le to | | | | | |
| Outcomes | | CO1: Illustrates different components of computer, its Characteristics, generations and | | | | | | | | |
| Outcomes | | application. Explain different number system used in computer system and binary arithmetic. | | | | | | | | |
| | | CO2: Introduce computer memory and I/O devices. Explain different computer | | | | | | | | |
| | | LO2: Introduce computer memory and I/O devices. Explain different computer languages and types of computer operating system. | | | | | | | | |
| | | scusses DOS history | | | manda Intra | duca foot | surge of MS | | | |
| | | its usage. | and variou | s DOS COIII | manus. muc | duce lead | ules of MS | | | |
| | | C | aat and wanic | over a veral form | otions Eval | oin was of | FMC Dovice | | | |
| | | roduce excel worksh | eet and vario | ous excel lui | icuons. Expi | iam use of | MS-Fower | | | |
| F ' ' | 1 | MS-Access. | <u> </u> | | | | | | | |
| Examination | I neory/ F | Practical/ Theory + P | таспсаг | | | | | | | |
| Mode | Written | A asi annount / | MCE | MTD | ECE | EDD | ADI /DDI | | | |
| Assessment | | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | | |
| Tools | Quiz | Project Work | 250/ | | 500/ | | 5 0/ | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | 5% | | | |
| Syllabus | | | | | | | CO | | | |
| TT '4 1 | T | | 0 NI . 1 | C | | | Mapping | | | |
| Unit 1 | | entals of Computer | | | | | CO1 | | | |
| • | Block Str | ructure of a Compu | ter, Characte | eristics of C | omputers, C | omputer | | | | |
| | generation | ns, Applications of C | Computers. C | lassification | of Compute | rs on the | | | | |
| | Basis of s | size and chronology. | | | | | | | | |
| • | Bit, byte. | binary, decimal, hex | adecimal, an | d octal syste | ms, conversi | on from | | | | |
| | | m to the other repres | | • | | | | | | |
| • | | ddition, subtraction | | | | | | | | |
| Unit 2 | | g System, Memory | | | Devices | | CO2 | | | |
| • | | OM, Cache and Seco | | | Devices | | | | | |
| • | | | · | • | N 000 | D 01/D | | | | |
| | MICR. | rices: Keyboard, Mo | use, Light po | en, Joystick, | Mouse, OC | R, OMR, | | | | |
| | | 1 | , | | 1. 1 | | | | | |
| | _ | levices: Monitor, I | - | - | - | | | | | |
| _ | Drumprin | nter, Dot Matrix prin | ter, ink jet pi | inter and La | iser printer, j | oiotters. | | | | |
| • | Machine | language, assembly | language, l | nigher level | language, 4 | GL and | | | | |
| | | ion to Compiler, Inte | | _ | | | | | | |
| • | | ulti programming, ti | • | | sor operating | g system. | | | | |
| | | d real time operating | _ | _ | - | | | | | |
| Unit 3 | | erating System & M | | | | | CO3 | | | |
| • | _ | story, Internal and Ex | | nands, Batcl | n Files | | | | | |
| • | | eatures of MS WORI | | | | ing files | | | | |
| | | g pages, paragraphs | | - 1 | | _ | | | | |
| | | ists and numbering. | | | | | | | | |
| | | | | | | | | | | |
| | and replacing text, inserting page breaks, page numbers, symbols, images | | | | | | | | | |

| | and dates. Using tables, header, footer. Using mail merge features. | |
|---------------------|---|-----|
| Unit 4 | MS Excel, MS PowerPoint and MS Access | CO4 |
| • | Excel Worksheet, Data Entry, Editing, Cell Addressing Ranges, Copying & Moving Cell Content, Inserting and Deleting Rows and Column, Column Formats, Printing, Creating, displaying charts, Working with functions - Date and time function, Statistical function, Mathematical and Trigonometric functions, Text function, Logical functions. | |
| • | Presentation overview, entering information, Presentation creation, opening and saving presentation, using transitions and animations. | |
| • | Creating a Database using MS Access, Basic Tables, Using Queries, Using the Auto Form Feature, Form Design, Using the Auto Report Feature, Report Design, Copying Data, Freezing Columns, Printing Tables, Printing Reports, Sorting Records, Using the Filter Sorts, Renaming Columns. | |
| Reference Book/s | Sinha, P.K.and Sinha,P., Foundations of Computing. NewDelhi: BPB First Edition, 2002. Norton Peter, Introduction to Computers, McGraw Hill. Rajaraman V, Fundamentals of Computers, New Delhi: Prentice Hall of India, Second Edition,1996. Jain Satish, MS Office 2010 Training Guide, Delhi: BPB Publications, 2010 Shelly G.B, Cashman Thomas J., and Verma at Misty E., Microsoft Office Word 2007: Complete Concepts and Techniques, New Delhi: Cengage Learning, 2007 Subramanian N, Introduction to Computers, Noida, UP, India: Tata McGraw Hill,1989 Cyganski D, Orr J A, Information Technology Inside and Outside, New Jersey USA: Pearson Education 2002. | |



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 2 | 4 |

| Course Code | CSP103 | | | | | | | | | |
|--------------|-----------------|--|---------------|---------------|-----------------|----------|-------------|--|--|--|
| Course Title | Algorithr | n Design and Progra | amming Us | ing C | | | | | | |
| Course | | mpletion of the cours | | _ | e to | | | | | |
| Outcomes | CO1: To o | define the concept of p | oroblem solv | ing and step | s to solving p | roblems | in computer | | | |
| | | n are using algorithr | | | | | | | | |
| | repetition s | | | | | | | | | |
| | CO2: To | CO2: To understand the Concept of fundamentals of programming & Control structure. | | | | | | | | |
| | CO3: App | CO3: Apply the concepts of Function, arrays, Structure & Union. | | | | | | | | |
| | CO4: Den | nonstrate the ability to | write C progr | ams using po | inters and file | handling | | | | |
| Examination | Theory/ P | Practical/ Theory + Practical/ | actical | | | | | | | |
| Mode | | | | | | | , | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | | |
| Tools | Quiz | Project Work | | | | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | 5% | | | |
| Syllabus | | | | | | | CO | | | |
| | | | | | | | Mapping | | | |
| Unit 1 | | entals of algorithm | | ogramming | , Operation | ns and | CO1 | | | |
| | | ons & Control Struc | | | | | | | | |
| • | | problem-solving, Pr | | | | | | | | |
| | | ming, Divide & Co | | | _ | • | | | | |
| | | Analyze Problem, Ex | - | ion), Algorit | thms and Flo | wcharts | | | | |
| | <u> </u> | ns, Symbols), pseudo | | | | | | | | |
| • | | Set, Identifiers ar | - | | | | | | | |
| | | , Expressions, Statem | nents, Symb | olic Constai | nts and Oper | ators & | | | | |
| | its types. | | | | | | | | | |
| • | _ | aracter Input, Single | | | 0 1 | | | | | |
| | | an Functions, Writing | | | out Print Fu | nctions, | | | | |
| | Gets and l | Puts Functions, Libra | ry functions | • | | | | | | |
| Unit 2 | Decision | Making and Loopin | g Statemen | ts & Array | | | CO2 | | | |
| • | | on, Decision Making | | | | | | | | |
| | | d Do-While, For Loo | p, Jump Stat | tements: Bre | ak, Continue | , Go to, | | | | |
| | Switch St | | | | | | | | | |
| • | | on to Arrays, Array | | | | | | | | |
| | | Iemory Representat | ion, Matri | ces, Strings | s, String H | andling | | | | |
| | Functions | | | | | | G0.2 | | | |
| Unit 3 | ł | s, Structure and Uni | | | | | CO3 | | | |
| • | | on To Functions, F | | | | | | | | |
| | | Functions, Parameter | | | · . | | | | | |
| | | e, Recursion, Global a | | | | | | | | |
| • | | on of Structure, A | | | , | tructure | | | | |
| | Initializati | ion, Arrays of Structu | ire, Nested S | Structures, U | Inions. | | | | | |
| Unit 4 | | Files & Preprocesso | | | | | CO4 | | | |
| • | Introducti | on To Pointers, Add | dress Opera | tor and Poi | nters, Decla | ring and | | | | |

| | Initializing Pointers, Assignment through Pointers, Pointers and Arrays. | | | | | | | |
|-------------|---|--|--|--|--|--|--|--|
| • | Introduction, creating a Data File, Opening and Closing a Data File, | | | | | | | |
| | Processing a Data File. | | | | | | | |
| • | Introduction and Use, Macros, Conditional Preprocessors, Header Files | | | | | | | |
| Text Book/s | 1. Balagurusami E, Programming in ANSIC, New Delhi: Tata Mc Graw Hill, | | | | | | | |
| | Fourth Edition (2010). | | | | | | | |
| Reference | 1. Sprankle, M&J. Hubbard, Problem solving and programming concepts, | | | | | | | |
| Book/s | 9 th Edition. NJ: Prentice Hall, 2012. | | | | | | | |
| | 2. Gaddis, T., <i>Starting out with programming logic and design</i> , 3 rd Edition. | | | | | | | |
| | Boston: Addison Wesley 2012. | | | | | | | |
| | 3. Venit, S. &E. Drake, <i>Prelude to programming: Concepts and design</i> , 5 th | | | | | | | |
| | Edition. Boston: Addison Wesley, 2011. | | | | | | | |
| | 4. R.G.Dromy. <i>How to Solve it by Computer</i> , 3 rd Edition, New Delhi: | | | | | | | |
| | Pearson Education, 2007. | | | | | | | |
| | 5. Kanetkar Yashvant P, <i>Let us C</i> , New Delhi: BPB Publications, Seventh | | | | | | | |
| | Edition (2007). | | | | | | | |
| | 6. Kernighan & Richie, <i>The C Programming Language</i> , New Delhi: PHI | | | | | | | |
| | Publication, Second Edition (2009). | | | | | | | |

Course Title: Office Automation Laboratory Course Code: CSP102

| L | T | P | Credits | Marks |
|---|---|---|---------|-------|
| 0 | 0 | 2 | 1 | 50 |

- Working of DOS internal & external commands.
- Learning to use MS WORD, MS EXCEL.
- Using MS PowerPoint to make slides and presentations.
- Introduction to the Database Window, Database Objects, Database Terminology
- Creating a Database using MS Access, Basic Tables
- Using Queries, Using the Auto Form Feature Form Design
- Using the Auto Report Feature, Report Design
- Copying Data, Freezing Columns
- Printing Tables, Printing Reports
- Sorting Records, Using the Filter Sorts, Renaming Columns

Course Title: C Programming Laboratory Course Code: CSP103

| L | T | P | Credits | Marks |
|---|---|---|---------|-------|
| 0 | 0 | 2 | 1 | 50 |

Implementation of C programming concepts:

- Control Structures, Loops, Arrays, Strings
- Functions, Structures, Union, Files, etc.



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 2 | 4 |

| Course Code | CSP104 | | | | | | | | |
|--------------|-------------|--|--|-----------------|-----------------|-------------|---------------|--|--|
| Course Title | Object O | riented Programm | ing using C | ++ | | | | | |
| Course | | mpletion of the cou | | | le to | | | | |
| Outcomes | | cuss the concepts of C | | | | eveloped la | inguages. | | |
| | | | ing the concepts of Classes and object by using real-world examples. | | | | | | |
| | | lement the concepts o | | | • | 1 | | | |
| | _ | eloping the programs | | | | nd using th | ne concept of | | |
| | file handli | | C | • | | C | • | | |
| | | raction with the IDE a | and help in un | derstanding t | he concept of | OOPs. | | | |
| Examination | | Practical/ Theory + P | | | • | | | | |
| Mode | | , | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | |
| Tools | Quiz | Project Work | | | | | | | |
| Weightage | 10% | 10% | 25% | _ | 50% | - | 5% | | |
| Syllabus | | | | l | l | I | CO | | |
| v | | | | | | | Mapping | | |
| Unit 1 | Introduc | tion to OOPS & Cl | ass Concept | s | | | CO1,5 | | |
| • | | Of OOP, OOP Feat | | | ics of Object | -Oriented | , | | |
| - | | Objects, Classes, In | | | | | | | |
| | | nism, Overloading, Co | | • | | J1 , | | | |
| • | | Objects, Inline Function | | | and Member F | Functions, | | | |
| | | ors and Destructors. | | | | | | | |
| • | | Objects, Array of P | | | | | | | |
| | | Local and Global | Class, Neste | d and Empty | Class, Pre- | processor | | | |
| TT 1: 0 | | Namespace. | 7. | | | | ~~ | | |
| Unit 2 | Console I/ | O &Operator Overl | oading | | | | CO2 | | |
| | Liororoby | of Concolo Stroom Cl | assas Unform | ottod and For | motted I/O Or | narotions | | | |
| • | Manipulate | of Console Stream Cla | asses, Unioni | iatied and Foi | | peranons, | | | |
| • | | able Operators, Ove | rloading-Un | ary and Rina | rv Arithmet | ic and | | | |
| · · | | l Operators, Overloa | | | | | | | |
| | | Delete Operators. | iding buosei | ipt, riitay, ii | inscrition, Ext | raction, | | | |
| Unit 3 | | unction and Type (| Conversion | & Inheritan | nce | | CO3 | | |
| • | | unction, Function C | | | | through | | | |
| | Friend Fu | | , on outling, | o , ci i oudili | 5 Operations | anough | | | |
| • | | pe Conversion, Con | nversion Ref | ween Ohie | ets and Racio | r Types | | | |
| • | | on Between Objects | | | and Dash | c Types, | | | |
| • | | n Rules, Different Fo | | | es of Constru | etors and | | | |
| • | | rs in Inheritance | | mance, Kole | | ciors and | | | |
| Unit 4 | | Sunctions & File Ha | andling | | | | CO4 | | |
| • | | Functions and The | | Pure Virtua | l Function | Virtual | | | |
| | , ii tuui I | distroits and The | 110000, | 1111110 | | , 11 tuul | | | |

| | Destructor, Virtual Derivation, Abstract Class. | | | | | | | | |
|-------------|--|--|--|--|--|--|--|--|--|
| • | ierarchy of File Stream Classes, Opening and Closing Files. | | | | | | | | |
| • | File Modes, Testing for Errors, File Pointers and Their Manipulations, | | | | | | | | |
| | ASCII & Binary Files, Sequential and Random-Access Files | | | | | | | | |
| Text Book/s | 1. Balaguruswami E, <i>Object Oriented Programming In C++</i> , New Delhi: | | | | | | | | |
| | Tata Mc Graw Hill,2006 | | | | | | | | |
| Reference | 1. Stroustrup Bjarne, The C++ Programming Language, New Delhi: | | | | | | | | |
| Book/s | Addison-Wesley Professional,2000 | | | | | | | | |
| | 2. Lafore Robert, <i>Object Oriented Programming in C++</i> . Delhi: Sams | | | | | | | | |
| | Publishing, 2000 | | | | | | | | |
| | 3. Lippman, Tom Weiss, C++ Primer, New Delhi: Addison Wesley, 2005 | | | | | | | | |
| | 4. Scildt Herbert, C++ The Complete Reference, New Delhi: Tata Mc Graw | | | | | | | | |
| | Hill, 2007 | | | | | | | | |



| L | T | P | Credit |
|---|---|---|--------|
| 1 | 0 | 2 | 2 |

| Course Code | CSP105 | | | | | | | | | |
|--------------|---------------|---|----------------|---------------|--------------|------------|---------|--|--|--|
| Course Title | Web Des | igning | | | | | | | | |
| Course | | empletion of the cou | irse the stude | nt will be ab | le to | | | | | |
| Outcomes | | oduce the creation | | | | | | | | |
| | | ng PHP for back-er | | | | S. | | | | |
| | | orking with PHP for | | | | | | | | |
| | | D4: Publishing web sites. | | | | | | | | |
| Examination | | heory/ Practical/ Theory + Practical | | | | | | | | |
| Mode | | , | | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | | |
| Tools | Quiz | Project Work | | | | | | | | |
| Weightage | 10% | 10% | 25% | _ | 50% | _ | 5% | | | |
| Syllabus | | 1 - 4 / 4 | | L | | | CO | | | |
| 25 2200 020 | | | | | | | Mapping | | | |
| Unit 1 | Introduc | tion to Web Devel | opment &H' | TML/DHTN | /IL | | CO1 | | | |
| • | + | Webpage, Static W | - | | | | | | | |
| • | | Basics, HTML Eler | | | | Program, | | | | |
| | | s, Headings, Paragra | | | | - | | | | |
| | | Forms, Frames, Where to put Tables, Lists, Images, Forms. | | | | | | | | |
| • | | CSS in DHTML, Implementation of Web Pages using CSS | | | | | | | | |
| Unit 2 | | ntroduction to PHP | | | | | | | | |
| • | Introducti | Introduction to PHP, PHP Environment, Syntax Overview, Variable Types. | | | | | | | | |
| • | Decision | Making, Control | Statements, | Arrays, Stri | ings, Functi | ions and | | | | |
| | Objects | C , | | • | | | | | | |
| | o ojevis | | | | | | | | | |
| Unit 3 | PHP form | ns and manipulati | ng files and | Connectivity | y | | CO3 | | | |
| • | | with Forms, Web C | | | | Cookies | | | | |
| | and Sessi | | 1 / | , | J | | | | | |
| • | Working | with Files, Opening | g, closing, co | oing, renami | ng and delet | ing a | | | | |
| | _ | uploading and down | | - | _ | _ | | | | |
| | with PHP | | C , | C | C | C | | | | |
| • | Database | Connectivity with | MySOL, per | forming bas | ic operation | s (insert. | | | | |
| | | Database Connectivity with MySQL, performing basic operations (insert, delete, update, select). | | | | | | | | |
| Unit 4 | , . | Purchasing a Domain Name & Web Space | | | | | CO4 | | | |
| • | | Name & Web Space | | | e & Web Spa | ace | | | | |
| | | e or Free), | | | 1 | | | | | |
| • | | g the Website to Re | emote Server | • | | | | | | |
| | 1 | | <u> </u> | | | | | | | |
| | | | | | | | | | | |
| Reference | | l Thomas, HTML o | | Complete Re | ference, Ne | w Delhi: | | | | |
| Book/s | Mc Gra | aw-Hill, Fifth Edition | on (2010). | | | | | | | |

| 2. Andy Harris, <i>HTML</i> , <i>XHTML</i> and <i>CSS Al l in One For Dummies</i> , Delhi: Willey, Second Edition (2010). | |
|---|--|
| 3. Lerdorf Rasmus, Tatroe Kevin, Mac In tyre Peter, <i>Programming PHP</i> , Delhi: O' Reilly Media, 2013. | |
| 4. Ullman Larry, <i>PHP for the World Wide Web, Visual Quick Start Guide</i> . New Delhi: Peachpit Press, fourth edition (2011) | |

Course Title: Web Designing Laboratory

Course Code: CSP105

| L | , | T | P | Credits | Marks |
|---|---|---|---|---------|-------|
| 0 | | 0 | 2 | 1 | 50 |

• Web designing using HTML, DHTML, CSS, and PHP.

Course Title: Object Oriented Programming Structures Laboratory

Course Code: CSP104

| L | T | P | Credits | Marks |
|---|---|---|---------|-------|
| 0 | 0 | 2 | 1 | 50 |

- Implementation of OOP concepts using C++
- Write program in 'C++' language
- Using input and output statements
- Using control statements.
- Using functions.
- Using array
- Using Classes and implementation of Constructor and Destructor.
- Using files.
- Using OOP's Concepts (Inheritance, Polymorphism, Encapsulation, Friend and Static Functions, Exception Handling)

Semester - 3



| L | T | P | Credit |
|---|---|---|--------|
| 4 | 0 | 0 | 4 |

| Course Code | CSP201 | | | | | | |
|-----------------|--|--|----------------|--------------|----------------|-------------|--------------|
| Course Title | Computer Oriented Numerical and Statistical Techniques | | | | | | |
| Course | | On the completion of the course the student will be able to | | | | | |
| Outcomes | | CO1: Understand various significant and fundamental concepts to inculcate in the students | | | | | |
| | | ate understanding of | f the applicat | ion of Nume | rical Algoritl | hms and Sta | atistical |
| | Methods. | | | | | | |
| | | derstand and learn n | | | • | | |
| | | derstand the implem | | | _ | a computer | and learning |
| | _ | errors in Numerica | | - | _ | | |
| | | derstand and Learn | Statistical me | ethods and T | echniques. | | |
| Examination | Theory | | | | | | |
| Mode | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL |
| Tools | Quiz | Project Work | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | |
| Syllabus | | | | | | | CO |
| | | | | | | | Mapping |
| Unit 1 | Errors a | nd Sources of Pro | pagation fo | or Errors (0 | 8 Hours) | | CO1 |
| • | Approxir | mations and Round | d-Off Errors | s: | | | |
| | | Floating Poin | nt Representa | tion of Numb | pers | | |
| | | Arithmetic O | | | | oint | |
| | | Numbers and | | | C | | |
| | | • Error in Num | _ | | | | |
| | | Pitfalls in Cor | - | | | | |
| • | Iterative | e Methods | p www.g | | | | |
| | | Zeros of a Sir | ngle Transce | endental Fai | iation and 7 | eros of | |
| | | Polynomial u | - | _ | | C1 03 01 | |
| | | False Positio | | on Method | | | |
| | | | | 4 | | | |
| Unit 2 | Colution | Newton Raple of Simultaneous I | | | | | CO2 |
| | Solution | of Simultaneous L | | | bursj | | CO2 |
| • | | Gauss Elimin | iation Method | 1 | | | |
| | | • Pivoting | | 1 5 0 | 000 | | |
| | | ILL Condition | - | | ement Of Sol | utions | |
| | | Gauss Siedel | Iterative Met | thods | | | |
| • | Numeric | Differentiation a | and Integra | tion | | | |
| | | Numerical D | ifferentiatio | n Using Inte | rpolation M | ethod | |
| | | Numerical In | ntegration, T | rapezoidal I | Rule | | |
| | Numerical Integration, Trapezoidal Rule Simpson's 1/8 Rule, Simpson 3/8 Rule. | | | | | | |
| | | • Simpson's 1/8 Rule, Simpson 3/8 Rule. Numerical Solution of Ordinary Differential equations (07 Hours) CO3 | | | | | |

| • | Euler Method | | | | | | |
|-------------|--|-----|--|--|--|--|--|
| | Runga Kutta Method | | | | | | |
| | Predictor Corrector Method. | | | | | | |
| • | Introduction to Statistics | | | | | | |
| | Meaning, Scope, Collection and Classification of Data. | | | | | | |
| | Methods to Measures Central Tendency | | | | | | |
| Unit 4 | Dispersion | CO4 | | | | | |
| • | Meaning | | | | | | |
| | Measurement of Dispersion (Mean Deviation, Standard Deviation and | | | | | | |
| | Variance) | | | | | | |
| • | Bivariate Data | | | | | | |
| | Correlation, Meaning, Type of Correlation, Correlation and Causation, Methods of Studying Correlation, | | | | | | |
| | Algorithm to Compute Karl Pearson's Correlation and Rank Correlation. Applications Based On Correlation. | | | | | | |
| | | | | | | | |
| Text Book/s | Rajaraman V, Computer Oriented Numerical Methods, Prentice Hall, India, 1993 | | | | | | |
| Reference | 1) Gupta S.C, Fundamental of Statistics, Himalayas Publication House, 2007 | | | | | | |
| Book/s | 2) Gupta &Kapoor, Applied Statistics, Sultan Chand & Sons, 2007 | | | | | | |
| | 3)Gupta S.P, Statistical Method, Sultan Chand & Sons, 2009 | | | | | | |
| | 4)Gupta, Rajesh Kumar. Numerical Methods: Fundamentals and | | | | | | |
| | Applications. United Kingdom, Cambridge University Press, 2019. | | | | | | |



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 2 | 4 |

| Course Code | CSP202 | | | | | | |
|--------------|---|---|-------------|----------------|---------------|--------------|---------------|
| Course Title | Object Oriented Programming using Java | | | | | | |
| Course | On the co | On the completion of the course the student will be able to | | | | | |
| Outcomes | CO1: Solve real world problems using OOP techniques. | | | | | | |
| | CO2: Solv | ve problems using jav | a collectio | n framework | and I/O clas | sses. | |
| | | lement Interfaces and | | | | | |
| | | elop multithreaded a | | | nization. De | evelop apple | ts for web |
| | applicatio | ns and able to design | GUI based | l application | | | |
| Examination | Theory + | Practical | | | | | |
| Mode | | T | 1 | | | | T |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL |
| Tools | Quiz | Project Work | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | |
| Syllabus | | | | | | | CO Mapping |
| Unit 1 | An Overv | view of JAVA (08 H | ours) | | | | CO1 |
| • | | of Java, Importance | | o Internet. Fe | eatures of I | AVA. | |
| | | , Object Oriented Ap | | | , | - , | |
| | | oata Types and Oper | • | e Conversion | n and Castin | ng. One | |
| | | onal and Multidimen | | | | <i>G</i> , | |
| | | Statements, Iterativ | | - | statements | S. | |
| Unit 2 | | | | //- F 8 | | | CO2 |
| • | Class Fun | damentals, Declaring | Objects, I | ntroducing M | ethods, Con | structors, | |
| | | ord, Overloading con | | | | | |
| | • | ses. Inheritance basic | | | | | |
| | | g and Abstract Classe | _ | | • | | |
| • | | and Interfaces, Acces | | n, Importing | Packages, In | nterfaces, | |
| | | Implementing, Apply | | | | | |
| | | Handling Fundamen | | | | | |
| | try and ca | tch, Creating own Ex | ceptions. | | | | |
| Unit 3 | | | | | | | CO3 |
| • | | ning The Java Thread | | | | | |
| | Inter threa | ad communication, Su | uspending, | Resuming an | d Stopping | Threads. | |
| • | | Basics, Streams, readi | _ | - | _ | - | |
| | PrintWriter class, Reading & writing Files, Byte Streams, Character Streams | | | | | | |
| | & Serialization. | | | | | | |
| Unit 4 | | | | | | | CO4 |
| • | | sics, Applet Architec | ture, Apple | t: Display, R | epaint, Parai | meter | |
| | | Event Handling: | | | | | |
| | _ | gation Event Model, l | Event Class | ses, Event Lis | tener Interfa | aces, AWT | |
| | Window | | | | | | |
| | | ntals, Working with F | Frame Wind | dows, Graphic | es, Color and | d Fonts. | |
| Practicals | | periments: | | | | | |
| | Task 1. In | heritance in JAVA | | | | | |

| | Task 2. Interfaces and Packages in JAVA | |
|-------------|--|--|
| | Task 3. Multithreading in JAVA | |
| | Task 4. Client –Server Networking | |
| | Task 5. Functional Programming, Pure functional programming- No State, | |
| | Immutable variables, favor recursion over looping. | |
| Text Book/s | Herbert Schildt (2019), Java The complete reference, 11th edition, Herbert | |
| | Schildt, McGraw Hill Education (India) Pvt. Ltd. | |
| Reference | S. Malhotra, S. Chudhary(2013), Programming in Java,, 2nd edition, Oxford | |
| Book/s | University Press | |
| | Liang, Y. D. (2018). Introduction to Java Programming and Data Structures: | |
| | Comprehensive Version. United Kingdom: Pearson. | |



| L | 1 | T | P | Credit |
|---|---|---|---|--------|
| 3 | | 0 | 2 | 4 |

| Course Code | CSP203 | | | | | | | |
|--------------|---|------------------------|---------------|--------------|-----------------|--------------|-------------|--|
| Course Title | Database Concepts | | | | | | | |
| Course | On the completion of the course the student will be able to | | | | | | | |
| Outcomes | | | | | | abase system | S. | |
| o ute office | CO1: To understand the basic concepts and the applications of database systems. CO2: To understand the basic concepts of data models and ER Diagrams. | | | | | | | |
| | | understand the relati | | | | | ization for | |
| | | pment of application | | | respies units a | ppij nomu | | |
| | | Master the basics of | | | es using SO | L. | | |
| Examination | Theory + | | | | <u> </u> | | | |
| Mode | , | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | |
| Tools | Quiz | Project Work | | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | | |
| Syllabus | | | • | . | • | • | CO | |
| | | | | | | | Mapping | |
| Unit 1 | An Overv | view of DBMS (08 | Hours) | | | | CO1 | |
| • | Concept | of File Processing S | ystems and | Database Sv | stems | | | |
| | | Administrator and | | | | | | |
| | | and Logical Data In | _ | | | | | |
| • | | vel Architecture o | | | | | | |
| | The External Level, Conceptual Level, The Internal Level | | | | | | | |
| Unit 2 | * | | | | | | CO2 | |
| • | | lationship Model, I | | | work and R | elational | | |
| | - | omparison of Netw | | | | | | |
| • | | e design and ER dia | | | | | | |
| | | s – Relationships a | | | | | | |
| | - | Design – Conceptua | | _ | _ | | | |
| Unit 3 | | l Databases (07 H | | | • | | CO3 | |
| • | | on, Terms a. Relat | | c. Attribute | d. Cardinalit | v e. Degree | | |
| | f. Domain | | 1 | | | , , | | |
| • | Keys (a) S | Super Key (b) Candi | idate Key (c) | Primary Ke | ey (d) Foreig | gn Key | | |
| • | | Algebra Operation | | | | | | |
| | | e (e.) Intersection (f | | | | • | | |
| Unit 4 | Relationa | l Database Design | (05 Hours) | | | | CO3 | |
| • | | on, Anomalies of u | | | Vormalizatio | on, | | |
| | Normal Forms: INF, 2NF, 3NF, BCNF, 4th NF, 5th NF | | | | | | | |
| • | Database Security, Integrity and Control | | | | | | | |
| Unit 5 | | uctured Query Lai | | | | | CO4 | |
| • | | on , History Of SQI | | | Commands | , DML | | |
| | | ls, DCL Command | | | | | | |
| | Functions | | | | | | | |
| • | Join Meth | ods, Union, Interse | ection, Minu | s, Views, Se | quences, Inc | dexing, | | |
| | Subquery | | | | • | - | | |
| Practicals | | periments: | | | | | | |

| | Task 1. Introduction to SQL and installation of SQL Server / Oracle. | |
|-------------|---|--|
| | Task 2. Data Types, Creating Tables, Retrieval of Rows using Select | |
| | Statement | |
| | Task 3. Conditional Retrieval of Rows, Alter and Drop Statements. | |
| | Task 4. Working with Null Values, matching a Pattern from a Table | |
| | Task 5. Ordering the Result of a Query, Aggregate Functions, Grouping the | |
| | Result of a Query, Update and Delete Statements. | |
| | Task 6. Set Operators, Nested Queries | |
| | Task 7. Joins, Sequences. | |
| | Task 8. Views, Indexes | |
| | Task 9. Database Security and Privileges: Grant and Revoke Commands, | |
| | Commit and Rollback Commands. | |
| Text Book/s | 1. Data base System Concepts, Silberschatz, Korth, McGraw hill, Sixth | |
| | Edition. | |
| | 2. Data base Management Systems, Raghurama Krishnan, Johannes Gehrke, | |
| | TATA McGrawHill 3rd Edition. | |
| Reference | 1. Fundamentals of Database Systems, Elmasri Navathe Pearson Education. | |
| Book/s | 2. An Introduction to Database systems, C.J. Date, A.Kannan, S.Swami | |
| | Nadhan, Pearson, Eight Edition for UNIT III | |
| | 3. Simplified Approach to DBMS– Kalyani Publishers | |



| L | T | P | Credit |
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| 3 | 0 | 2 | 4 |

| Course Code | CSP204 | | | | | | |
|---------------------|---|---|--|---|---|---------------------------------------|---------------|
| Course Title | Data Structures | | | | | | |
| Course Outcomes | On the completion of the course the student will be able to CO1: Student will be able to handle operation like searching, insertion, deletion, trave on various Data Structures; CO2: Student will able to write an algorithm Selection Sort, Bubble Sort, Insertion Sort, Quick Sort, Merge Sort; CO3: Students will be able to choose appropriate Data Structure as applied to specific problem definition; CO4: Implement Various searching algorithms and become familiar with their design methods. | | | | | | tion Sort, |
| Examination Mode | I neory ar | nd Practical | | | | | |
| Assessment Tools | Written Quiz | Assignment/ Project Work | MSE | MTP | ESE | EPR | ABL/PBL |
| Weightage | 10% | 10% | 25% | - | 50% | - | |
| Syllabus Unit 1 | | tion (08 Hours) | | | | | CO Mapping |
| • | Primitive and Composite Various Data Structures, Common Operations on Data Structures, Algorithm Complexity, Time-Space Tradeoff Between Algorithms, Complexity of Algorithms String: Strings as ADTs, Representation and Manipulation, String Operations. Arrays Arrays Arrays Defined, Representing Arrays in Memory, Various Operations on Linear Arrays. Bubble Sort. Linear Search, Binary Search Records, Matrices, Sparse Matrices | | | | | | |
| Unit 2 | Types of I linked List Collection Advantag Lists Stacks Description Linked Linked Link | Lists, Stacks, Quer Linked Lists, Represt, Searching in a line, Insertion and dele of Using Linked on of Stack Structures, Applications of ding algorithms. | esenting Link nked list, Men letion in a link Lists Over An | ed Lists in Mory Allocated list. Circurays, Varioutation of State | tion and Garb lar Linked I is Operation of the Using Arr | page List. on Linked ays and | CO2 |
| Unit 3 | | Trees, Graphs, H | eaps (08 Hou | urs) | | | CO3 |
| • | | , i / | | | | | |

| | | 1 |
|-------------------|---|-----|
| | Queues | |
| | Implementation of Queue Using Linked Lists, Circular Queues, De-Queues, | |
| | Priority Queues. | |
| • | Trees | |
| | Description of Tree Structure and Its Terminology, Binary Tree, | |
| | representation in memory, Traversing Binary Trees, Traversal Algorithms | |
| | using Stacks. | |
| • | Graphs | |
| _ | Representation of Graphs and Applications: Adjacency Matrix, Path Matrix | |
| | Warshall's Algorithm, Linked Representation of a Graph | |
| | | |
| | Traversing a Graph: DFS and BFS, Spanning Trees. | |
| | Heaps | |
| | Description of Heap Structure, Implementing Heaps Using Arrays | |
| Unit 4 | Searching and Sorting Algorithms (08 Hours) | CO4 |
| • | Linear Search, Binary Search | |
| | Insertion Sort, Selection Sort, Bubble Sort, radix Sort, Merge Sort, Quick Sort | |
| | Files | |
| | Operations on Files, Types of Files | |
| | File Organizations: Sequential Files, Indexed Sequential File, Directed Files | |
| | • | |
| D 41 1 | and Multikey Files | |
| Practical: | List of Experiment: | |
| | Task 1: Write a program to insert a new element at end as well as at a given | |
| | position in an array. | |
| | Task 2: Write a program to delete an element from a given array whose value | |
| | is given or whose position is given. | |
| | Task 3: Write a program to find the location of a given element using Linear | |
| | Search. Task 4: Write a program to find the location of a given element using | |
| | Binary Search. Task 5: Write a menu driven program to perform following | |
| | insertion operations in a single linked list: i. Insertion at beginning ii. | |
| | Insertion at end iii. Insertion after a given node iv. Traversing a linked list | |
| | Task 6: Write a program to implement push and pop operations on a stack | |
| | | |
| | using linear array. | |
| | Task 7: Write a program to convert an infix expression to a postfix expression | |
| | using stacks. | |
| | Task 8: Write a program to evaluate a postfix expression using stacks. | |
| | Task 9: Program to sort an array of integers in ascending order using bubble | |
| | sort. | |
| | Task 10: Program to sort an array of integers in ascending order using | |
| | selection sort | |
| | Task 11: Program to traverse graphs using BFS. | |
| | Task 12: Program to traverse graphs using DFS. | |
| Text Book/s | "Data Structures with C (Schaum's Outline Series)", Seymour Lipschutz, 1st | |
| IOA BOOK S | edition, McGraw Hill Education | |
| Reference | | |
| | 1) "Fundamentals of Data Structures", Illustrated Edition by Ellis Horowitz, | |
| Book/s | Sartaj Sahni, Computer Science Press. | |
| | 2) Algorithms, Data Structures, and Problem Solving with C++", Illustrated | |
| | Edition by Mark Allen Weiss, Addison-Wesley Publishing Company. | |
| | 3) "Classic Data Structures", Samanta and Debasis, 2nd edition, PHI | |
| | publishers. | |
| | | |
| | | • |



| L | T | P | Credit |
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| Course Code | CSP205 | | | | | | | |
|--------------|---|---|-----------------------------------|----------------|----------------|-------------|---------|--|
| Course Title | Compute | r Graphics | | | | | | |
| Course | On the completion of the course the student will be able to | | | | | | | |
| Outcomes | CO1: To | implement various | rt the basic g | eometrical p | rimitives, | | | |
| | | ations, Area filling, | | | | • | | |
| | | describe the import | 11 0 | limensional 1 | transformatio | n and viewi | ng. | |
| | | describe the importa | | | | | U | |
| | | nderstand color mo | | | | C | | |
| Examination | Theory + | | • | | | | | |
| Mode | | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | |
| Tools | Quiz | Project Work | | | | | · | |
| Weightage | 10% | 10% | 25% | _ | 50% | _ | | |
| Syllabus | 10,0 | 1070 | 1 =0 70 | <u> </u> | 10070 | I | СО | |
| | | | | | | | Mapping | |
| Unit 1 | Overview | of Computer Gra | phics (08 H | ours) | | | CO1 | |
| • | | Computer Graphics | | | | | | |
| | | splay devices, | ,, 1 1 pp11 0 at10. | , | | | | |
| | | | Controller I | Display Proce | essor Rando | m_Scan | | |
| | Raster–Scan displays: Video Controller, Display Processor, Random–Scan displays, Color CRT Monitors; Common Graphic Input devices, Graphics | | | | | | | |
| | Software's | | | | | | | |
| • | Output Primitives: | | | | | | | |
| | Line Drawing: DDA, Bresenham Line Algorithm; | | | | | | | |
| | Midpoint Circle drawing algorithms; Flood and Boundary Filling Algorithms. | | | | | | | |
| | | | | | | | | |
| Unit 2 | Two-Dimensional Geometric Transformation and Viewing (08 Hours) | | | | | | | |
| • | | Basic transformations: Translation, Rotation, Scaling, Reflection, Shearing | | | | | | |
| | | | | - | | mearing | | |
| | Matrix representations and Homogenous Coordinates; Composite transformations: Translations, Rotations, Scaling. | | | | | | | |
| • | Two-Dimensional Viewing: Viewing coordinate reference frame; Window to | | | | | | | |
| | | | | ordinate reres | rence trame, | Willdow to | | |
| | Viewport coordinate transformation. Point Clipping, Line Clipping : Cohen–Sutherland, Liang– Barskey | | | | | | | |
| | Algorithms for line clipping; text Clipping; | | | | | | | |
| Unit 3 | Three Dimensional Transformations & Viewing (08 Hours) | | | | | | CO3 | |
| • | | on, Rotation, Scaling | | | | ations | CO3 | |
| | | nd Perspective Proje | J, | | | | | |
| | | umes and Clipping. | | vilig Trails10 | illiation. Vie | w Flaii, | | |
| • | | urface Detection N | | | | | | |
| | | e Detection | Temous | | | | | |
| | | | | | | | | |
| | | ffer Method | | | | | | |
| | A-Buffer Method Scan-Line Method | | | | | | | |
| IInit 4 | | | | | | | CO4 | |
| Unit 4 | Color Mc | odels (08 Hours) | | | | | CO4 | |

| • | Color Models: Properties of Light, Intuitive Color Concepts, concepts of | | | | |
|-------------|--|--|--|--|--|
| | chromaticity, RGB Color Model, CMY Color Model, HLS and HSV Color | | | | |
| | Models, Conversion between RGB and CMY color Models, Conversion | | | | |
| | between HSV and RGB color models, Color Selection and Applications. | | | | |
| Practical: | List of Experiment: | | | | |
| | Task 1. WAP to draw different geometric structures using different functions. | | | | |
| | Task 2. Implement DDA line generating algorithm. | | | | |
| | Task 3. Implement Bresenham's line generating algorithm. | | | | |
| | Task 4. Implement Mid-point circle line generating algorithm. | | | | |
| | Task 5. Implementation of Bresenham's circle drawing algorithm. | | | | |
| | Task 6. Implementation of mid-point circle generating Algorithm. | | | | |
| | Task 7. WAP of color filling the polygon using Boundary fill and Flood fill | | | | |
| | algorithm. Task 8. To translate an object with translation parameters in X and | | | | |
| | Y directions. | | | | |
| | Task 9. Program of line clipping using Cohen-Sutherland algorithm. | | | | |
| | Task 10. To perform composite transformations of an object. | | | | |
| Text Book/s | D. Hearn and M.P. Baker, Computer Graphics: C version | | | | |
| Reference | 1) D.F. Rogers, Procedural Elements for Computer Graphics, 2nd Edition, | | | | |
| Book/s | Addison Wasley | | | | |
| | 2) J.D. Foley et al, Computer Graphics, Principles and Practices, 2nd Edition, | | | | |
| | Addison Wasley | | | | |
| | 3) Roy A. Plastock, Gordon Kalley, Computer Graphics, Schaum's Outline | | | | |
| | Series | | | | |



| L | T | P | Credits |
|---|---|---|---------|
| 3 | 0 | 0 | 3 |

| CSP206 | | | | | | |
|---|---|--|--|--|---|---|
| Operating | Systems | | | | | |
| CO1-To understanding CPU Scheduling, Synchronization, Deadlock Handling and CO2-Comparing CPU Scheduling Algorithms. Solve Deadlock Detection Problems. CO3-To describe the role of paging, segmentation and virtual memory in operating systems. CO4-To defining I/O systems, Device Management Policies and Secondary Storage Structure and Evaluation of various Disk Scheduling Algorithms. | | | | | | |
| Theory+ I | Practical | | | | | |
| Written Ouiz | Assignment/ Project Work | MSE | MTP | ESE | EPR | ABL/PBL |
| | 10% | 25% | - | 50% | _ | |
| | | | 1 | 1 | 1 | CO Mapping |
| Introduction to Operating System (15 Hours) | | | | | | CO1 |
| OS, History of OS, Types of OS Functions/operations of OS, User services/jobs, system calls Traps, architectures for operating systems Process Management Process overview, Process states Interrupt mechanism | | | | | | |
| CPU Scheduling and Process Synchronization(18 hours) Scheduling algorithms Pre-emptive scheduling & Non-Pre-emptive scheduling Levels of schedulers Process Synchronization, Critical section and mutual exclusion problem Classical synchronization problems, Multithreading. System Deadlock Deadlock characterization, Deadlock prevention and avoidance Deadlock detection and recovery, practical considerations | | | | | CO2 | |
| | Operating CO1-To CO2-Con CO3-To systems. CO4-To Structure Theory+ I Written Quiz 10% Introduc Process M Schedulin Pre-empti | Operating Systems CO1-To understanding CPC CO2-Comparing CPU Schedu CO3-To describe the role of systems. CO4-To defining I/O systems. CO4-To defining I/O systems. Structure and Evaluation of vocation of voc | Operating Systems CO1-To understanding CPU Scheduling CO2-Comparing CPU Scheduling Algorith CO3-To describe the role of paging, so systems. CO4-To defining I/O systems, Device Structure and Evaluation of various Disk Structure and Evaluatio | Operating Systems CO1-To understanding CPU Scheduling, Synchron CO2-Comparing CPU Scheduling Algorithms. Solve D CO3-To describe the role of paging, segmentation systems. CO4-To defining I/O systems, Device Managemen Structure and Evaluation of various Disk Scheduling A Theory+ Practical Written Assignment/ MSE MTP Quiz Project Work | Operating Systems CO1-To understanding CPU Scheduling, Synchronization, Dec CO2-Comparing CPU Scheduling Algorithms. Solve Deadlock Det CO3-To describe the role of paging, segmentation and virtual systems. CO4-To defining I/O systems, Device Management Policies a Structure and Evaluation of various Disk Scheduling Algorithms. Theory+ Practical Written Assignment/ MSE MTP ESE Quiz Project Work 10% 10% 25% - 50% Introduction to Operating System (15 Hours) OS, History of OS, Types of OS Functions/operations of OS, User services/jobs, system Traps, architectures for operating systems Process Management Process Management Process Overview, Process states Interrupt mechanism CPU Scheduling and Process Synchronization(18 hours) Scheduling algorithms Pre-emptive scheduling & Non-Pre-emptive scheduling | Operating Systems CO1-To understanding CPU Scheduling, Synchronization, Deadlock Hat CO2-Comparing CPU Scheduling Algorithms. Solve Deadlock Detection Probe CO3-To describe the role of paging, segmentation and virtual memory is systems. CO4-To defining I/O systems, Device Management Policies and Second Structure and Evaluation of various Disk Scheduling Algorithms. Theory+ Practical Written Assignment/ MSE MTP ESE EPR Quiz Project Work 10% 10% 25% - 50% - Introduction to Operating System (15 Hours) OS, History of OS, Types of OS Functions/operations of OS, User services/jobs, system calls Traps, architectures for operating systems Process Management Process Management Process Management Interrupt mechanism CPU Scheduling and Process Synchronization(18 hours) Scheduling algorithms Pre-emptive scheduling & Non-Pre-emptive scheduling |

| Unit 3 | Storage Management (15 Hours) Storage allocation methods: Single contiguous allocation, Multiple contiguous allocation | | | | |
|-------------|---|-----|--|--|--|
| | | | | | |
| | Memory Management Paging, Segmentation combination of Paging and Segmentation Virtual memory concepts, Demand Paging, Page replacement Algorithms Thrashing. Address Protection, Cache memory, hierarchy of memory types, associative memory. | | | | |
| Unit 4 | File Management (12 Hours) | CO4 | | | |
| | Overview of File Management System Disk Space Management, Directory Structures Protection Domains, Access Control Lists, Protection Models Queue management, File and directory systems Device Management Goals of I/O software, Design of device drivers, Device scheduling policies FCFS, SSTF, SCAN, CSCAN, LOOK, CLOOK | | | | |
| Text Book/s | 1. Galvin and Silberschatz A., <i>Operating System Concepts</i> , Eigth Addition, New York: J. Wiley & Sons, 2009. | | | | |
| Reference | | | | | |
| Book/s | Crowley, Operating Systems: A Design Oriented Approach, New Delhi: Tata McGraw Hill, 2008. Donovan J.J, Systems Programming, New York: McGraw Hill, 1972. Dhamdhere. D.M, System Programming and Operating Systems, New Delhi: Tata McGraw Hill, 1999. Madnick and Donovan, Operating System, New York: McGraw Hill, 1978. Beck Leland L., System Software, Delhi: Pearson Education, 2000. Henson P.B., Operating System Principles, Delhi: Prentice Hall Tenenbaum A.S., Operating System: Design and Implementation, New Delhi: PHI, 2013. Silberschatz, Abraham, et al. Operating System | | | | |



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 0 | 3 |

| Course Code | CSP207 | | | | | | | |
|--------------|---|---|--------------|------------|--------------|------------|---------|--|
| Course Title | | r Organization and | Architectu | re | | | | |
| Course | | On the completion of the course the student will be able to | | | | | | |
| Outcomes | | nonstrate the working | | | | C and CISC | | |
| | Architectu | | ,] | 6 | | | | |
| | CO2: Describe the operations and language for the register transfer, micro operations and | | | | | | | |
| | input- output organization. | | | | | | | |
| | CO3: Understand the organization of memory and memory management hardware. | | | | | | | |
| | | orate advanced conc | | | | | | |
| | processor | communication and s | synchroniza | tion. | | | | |
| Examination | Theory | | | | | | | |
| Mode | - | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | |
| Tools | Quiz | Project Work | | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | | |
| Syllabus | | | | | | | CO | |
| | | | | | | | Mapping | |
| Unit 1 | Digital Lo | ogic Circuit (08 Hou | ırs) | | | | CO1 | |
| • | Logic Gates, Boolean Algebra, Map Simplification, Combinational Circuits, | | | | | | | |
| | Flip Flops, Sequential Circuits | | | | | | | |
| • | Digital Components | | | | | | | |
| | Decoders, Multiplexers, Registers, Shift Registers, Binary Counters | | | | | | | |
| • | Data Representation | | | | | | | |
| | Data Types, Complements, Fixed-Point Representation, Floating-Point | | | | | | | |
| | Representation, Error Detection Codes | | | | | | | |
| Unit 2 | Register | Transfer and Micro | operations | (08 Hours |) | | CO2 | |
| • | Computer | Registers, Register T | ransfer, Bu | s and Memo | ry Transfers | S, | | |
| | Arithmetic | c Microoperations, Lo | ogic Micro | operations | • | | | |
| | | | | | | | | |
| | Addressii | | | | | | | |
| | | on & different types of | | | | | | |
| • | | mputer Organization | | | | | | |
| | | Instructions, Memor | - | | | • | | |
| | | n Codes, Instruction I | | | | | | |
| | Instructions, Zero Address, One Address, Two Address and Three Address | | | | | | | |
| | Instructions), Design of Accumulator Logic. | | | | | | | |
| Unit 3 | | tion to Computer O | | 1 (08 Hour | s) | | CO3 | |
| • | | on to Computer and C | CPU | | | | | |
| | Von Neun | nann Architecture. | | | | | | |
| | | | | | | | | |
| | | Organization | _ | | | | | |
| | Memory I | Hierarchy, Types of N | lemory | | | | | |
| | | | | | | | | |

| | Reduced Instruction Set Computer (RISC) | |
|-------------|--|-----|
| | CISC Characteristics, RISC Characteristics, RISC Instructions | |
| • | Microprogrammed Control | |
| | Control Memory, Address Sequencing, Microprogram Example, Design of | |
| | Control Unit | |
| Unit 4 | Input Output Organization (08 Hours) | CO4 |
| • | Input output Interface, Memory Mapped I/O; Interrupt | |
| | Asynchronous Data Transfer: Strobe Control, Handshaking | |
| | Priority Interrupts: Daisy-Chaining, Parallel Interrupt, Priority Encoder | |
| | Interrupt Cycle, Types of Interrupt: Program interrupt | |
| | Priority Interrupts, Direct Memory Access (DMA) | |
| | | |
| Text Book/s | Mano M.M., Computer System Architecture, Delhi: Prentice Hall of India | |
| Reference | 1) Mano M.M., Digital Logic and Computer Design, Delhi: Prentice Hall of | |
| Book/s | India. | |
| | 2) Hayes, Computer Architecture and Organization, New Delhi: McGraw Hill | |
| | International Edition. | |
| | 3) Tannenbaum A.S., Structured Computer Organization, Delhi: Prentice Hall | |
| | of India | |
| | 4) Brey B, The Intel Microprocessors, New Delhi: Pearson Education. | |
| | 5) Sloan M.E, Computer Hardware and Organization, 2nd Edition, New | |
| | Delhi: Galgotia, Pvt. Ltd | |
| | 6)Hennessy, John L., and Patterson, David A. Computer Architecture: A | |
| | Quantitative Approach. India, Elsevier Science, 2017. | |



| L | T | P | Credit |
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| 3 | 0 | 2 | 4 |

| Course Title | ls BL |
|--|----------|
| Outcomes CO1: Interaction with different hardware devices present in computer networks and discuss various network models. CO2: Interaction with data link layer and its protocols. CO3: Interaction various Routing algorithms. In addition to that functionality of network layer. CO4: Functionality of Transport layer and Implementation of Application layer protocolin real-world scenarios. Examination Mode Assessment Written Assignment/ MSE MTP ESE EPR ABL/Prols Quiz Project Work Weightage 10% 10% 25% - 50% - CO Mappi Unit 1 Introduction to Data Communication (08 Hours) CO1 Components of Data Communication, Data Representation | ls BL |
| discuss various network models. CO2: Interaction with data link layer and its protocols. CO3: Interaction various Routing algorithms. In addition to that functionality of network layer. CO4: Functionality of Transport layer and Implementation of Application layer protocologic in real-world scenarios. Examination Mode Assessment Written Assignment/ MSE MTP ESE EPR ABL/Prols Quiz Project Work Weightage 10% 10% 25% - 50% - COMappi Unit 1 Introduction to Data Communication (08 Hours) Components of Data Communication, Data Representation | ls BL |
| CO2: Interaction with data link layer and its protocols. CO3: Interaction various Routing algorithms. In addition to that functionality of network layer. CO4: Functionality of Transport layer and Implementation of Application layer protocol in real-world scenarios. Examination Mode Assessment Written Assignment/ MSE MTP ESE EPR ABL/Prols Quiz Project Work Weightage 10% 10% 25% - 50% - Syllabus CO Mappi Unit 1 Introduction to Data Communication (08 Hours) CO1 Components of Data Communication, Data Representation | ls BL |
| CO3: Interaction various Routing algorithms. In addition to that functionality of network layer. CO4: Functionality of Transport layer and Implementation of Application layer protocol in real-world scenarios. Examination Mode Assessment Written Assignment/ MSE MTP ESE EPR ABL/P CONSTRUCTION CONSTRUCT | ls BL |
| layer. CO4: Functionality of Transport layer and Implementation of Application layer protocol in real-world scenarios. Examination Mode Assessment Written Assignment/ MSE MTP ESE EPR ABL/P Tools Quiz Project Work Weightage 10% 10% 25% - 50% - Syllabus Unit 1 Introduction to Data Communication (08 Hours) COmponents of Data Communication, Data Representation | ls BL |
| CO4: Functionality of Transport layer and Implementation of Application layer protocol in real-world scenarios. Examination Mode Assessment Tools Quiz Project Work Weightage 10% 10% 25% - 50% - Syllabus Unit 1 Introduction to Data Communication (08 Hours) CO Components of Data Communication, Data Representation | BL |
| in real-world scenarios. Examination Mode Assessment Written Assignment/ MSE MTP ESE EPR ABL/P Tools Quiz Project Work Weightage 10% 10% 25% - 50% - Syllabus Unit 1 Introduction to Data Communication (08 Hours) CO COT COT COT COT COT COT COT COT COT | BL |
| Examination Mode Assessment Written Assignment/ MSE MTP ESE EPR ABL/P Tools Quiz Project Work Weightage 10% 10% 25% - 50% - Syllabus Unit 1 Introduction to Data Communication (08 Hours) CO Components of Data Communication, Data Representation | |
| Mode Assessment Written Assignment/ MSE MTP ESE EPR ABL/P Tools Quiz Project Work Weightage 10% 10% 25% - 50% - Syllabus Unit 1 Introduction to Data Communication (08 Hours) CO Components of Data Communication, Data Representation | |
| Assessment Tools Quiz Project Work | |
| Tools Quiz Project Work | |
| Weightage 10% 10% 25% - 50% - CO Mappi Unit 1 Introduction to Data Communication (08 Hours) CO1 Components of Data Communication, Data Representation | ng |
| Syllabus Unit 1 Introduction to Data Communication (08 Hours) CO1 Components of Data Communication, Data Representation | ng |
| Unit 1 Introduction to Data Communication (08 Hours) CO1 Components of Data Communication, Data Representation | ng |
| Unit 1 Introduction to Data Communication (08 Hours) CO1 • Components of Data Communication, Data Representation | ng —— |
| Components of Data Communication, Data Representation | |
| | |
| Transmission Impairments, Switching, Modulation, Multiplexing | |
| | |
| Review of Network Hardware: LAN, MAN, WAN | |
| Wireless networks, Internetworks | |
| Review of Network Software: Layer, Protocols, Interfaces and Services | |
| Review of Reference Models: OSI, TCP/IP and their comparison | |
| Physical Layer | |
| Transmission Media: Twisted pair, Coaxial cable, Fibre optics, | |
| Unit 2 | |
| | |
| Error Correction and Detection | |
| Framing, Noiseless Channels and Noisy Channels | |
| Multiple Access Protocol (ALOHA, CSMA, CSMA/CD, | |
| CSMA/CA) | |
| Wired LANs | |
| Unit 3 Network Layer (08 Hours) CO3 | |
| Logical Addressing, Internet Protocol IPv4 and IPv6 | |
| Design Issues, Routing Algorithms (Shortest Path, Flooding, | |
| Distance Vector, Hierarchical, Broadcast, Multicast) | |
| Internetworking, IP Protocol, ARP, RARP. | |
| Unit 4 Transport Layer (08 Hours) CO4 | |
| Flow Control, Buffering | |
| Internet Transport Protocol (TCP and UDP) | |
| Congestion Control Algorithms (Leaky bucket, Token bucket, | |
| Load shedding) | |

| | Application Layer | | | | | | |
|-------------|---|--|--|--|--|--|--|
| | Domain name system, Email, File transfer protocol | | | | | | |
| | HTTP, HTTPS, World Wide Web. | | | | | | |
| Practical: | List of Experiment: | | | | | | |
| | Task 1. Specifications of latest desktops and laptops. | | | | | | |
| | Task 2. Familiarization with Networking Components and devices: LAN | | | | | | |
| | Adapters, Hubs, Switches, Routers etc. | | | | | | |
| | Task 3. Familiarization with Transmission media and Tools: Co-axial cable, | | | | | | |
| | UTP Cable, Crimping Tool, Connectors etc. | | | | | | |
| | Task 4. Preparing straight and cross cables. | | | | | | |
| | Task 5. Study of various LAN topologies and their creation using network | | | | | | |
| | devices, cables and computers. | | | | | | |
| | Task 6. Configuration of TCP/IP Protocols in Windows and Linux. | | | | | | |
| | Task 7. Implementation of file and printer sharing. | | | | | | |
| | Task 8. Designing and implementing Class A, B, C Networks | | | | | | |
| | Task 9. Subnet planning and its implementation | | | | | | |
| | Task 10. Installation of ftp server and client | | | | | | |
| Text Book/s | Tanenbaum. Andrew S., Computer Networks, 4th Edition, New Delhi: PHI, 2013. | | | | | | |
| Reference | Forouzan B. A., Data Communications and Networking, Fourth | | | | | | |
| Book/s | Edition, New Delhi: Tata McGraw Hill, 2003. | | | | | | |
| | Stalling W, Data & Computer Communications, New Delhi: PHI, | | | | | | |
| | Ninth Edition 2010. | | | | | | |
| | Scott, Russell. Computer Networking: This Book Includes: Computer | | | | | | |
| | Networking for Beginners and Beginners Guide (All in | | | | | | |
| | One). N.p., Russell Scott, 2021. | | | | | | |

SEMESTER 5



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 2 | 4 |

| Course Code | CSP302 | | | | | | | |
|--------------|---|-----------------------|---|--------------|--------------|--------------|------------|--|
| Course Title | Programming in Python | | | | | | | |
| Course | CO1: To acquire programming skills in core Python. | | | | | | | |
| Outcomes | CO2: To acquire the skills of using operators and working with control constructs | | | | | | | |
| | in Python | | | | | | | |
| | CO3: To develop the skills of using data types and creating & designing functions & | | | | | | | |
| | | and object oriented p | | | | | | |
| | | acquire object orio | ented progra | ımming, File | e handling a | nd Exception | n Handling | |
| - | Skills in l | • | | | | | _ | |
| Examination | Theory + | Practical | | | | | | |
| Mode | *** | T | 1.605 | 1.000 | l nan | EDD | T | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | | |
| Tools | Quiz | Project Work | 250/ | | 500/ | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | | |
| Syllabus | | | | | | | | |
| Unit 1 | Introduc | ction to Python Lar | าฐบลฐอ | | | | | |
| 0.1110 1 | | • | | hon Origin | of Python | | | |
| | Programming language, History of Python, Origin of Python Programming, Features, Limitations, Applications, Getting and Installing Python, Python Environment Variables, Python Help, Python differences from other languages | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | 1 yulon unterences from other languages | | | | | | | |
| | Python Data Types and Input Output | | | | | | | |
| | Keywords, Identifiers, Variables, Statements, | | | | | | | |
| | | on, Documentation, 1 | | | , | | | |
| | | | Data Type, 1 | ype conven | 51011. | | | |
| Unit 2 | • | nput and Output. | | | | | | |
| Omt 2 | _ | | sianment I | ogical Dity | ica and Dut | hon anaoial | | |
| | Arithmetic, Comparison, Assignment, Logical, Bitwise, and Python special | | | | | | | |
| | operators. | | | | | | | |
| | Expressions, Precedence and Associativity | | | | | | | |
| | Control Structures | | | | | | | |
| | Decision Making Statements | | | | | | | |
| | Python Loops | | | | | | | |
| | Python Control Statements | | | | | | | |
| Unit 3 | • | | | | | | | |
| | | | Python Native Data Types Creation of following Data Types along with methods and functions | | | | | |
| | Creation of following Data Types along with methods and functions Number, String, Tuple | | | | | | | |

| | Set, Dictionary |
|-------------|--|
| | Python Functions and Modules |
| | Creating Functions, Advantages of Functions, Types of Functions, Built-In, User Defined Functions, Anonymous Functions, Call by assignment, Call by object Reference, Recursion, Designing of Modules, Importing Modules. |
| Unit 4 | Python Class and Objects |
| | Designing Classes, Creating Objects, Accessing |
| | Objects, init method, constructor, garbage collection, |
| | destroying objects, inheritance and operator overloading |
| | File Handling |
| | File creation, open() and close() methods, read() and write() methods, file modes, file encoding, file object attributes, renaming and deleting files, Python directory, directory methods and functions. |
| | Exception Handling |
| | Python Exception, Built-in Exception, Exception Handling, Try, except, finally, Python user defined exceptions |
| Practical: | |
| Text Book/s | |
| Reference | 1. M. C. Brown, The Complete Reference Python, Osborne/McGraw-Hill, 2001. |
| Book/s | S. Maruch, A. Maruch, Python for Dummies, John Wiley & Sons, 2011. A. B. Downey, Think Python, O'Reilly Media Inc., 2012. B. Slatkin, Effective Python, Addison Wesley Professional, 2015. J. M. Zelle, Python Programming: An Introduction to Computer Science, Franklin, Beedle & Associates, Inc., 2004. |



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 2 | 4 |

| Course Code | CSP303 | | | | | | | | |
|--------------|---|--|----------------|-----------------|---------------|---------------|----------|--|--|
| Course Title | Web Engineering using ASP.NET | | | | | | | | |
| Course | CO1: To explain the three pillars of object oriented programming. | | | | | | | | |
| Outcomes | CO2: To | develop working kn | owledge of S | Standard Cor | trols, valida | tion controls | and Rich | | |
| | controls. | | | | | | | | |
| | CO3: To learn to design Website with Master Pages, List Controls and Grid View Controls CO4: To learn to work with SQL Data Source Control and Building Data Access | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | Compone | Components with ADO.NET. | | | | | | | |
| Examination | Theory + | Practical | | | | | | | |
| Mode | | | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | |
| Tools | Quiz | Project Work | | | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | | | |
| Syllabus | | | | | | | CO | | |
| | | Мар | | | | | | | |
| Unit 1 | Introduc | | | | | | CO1 | | |
| | | of .NET Framework | | ~ ~ | | | | | |
| | The .NE | The .NET Framework Class Library, familiarization with visual studio.NET | | | | | | | |
| | IDE, Desi | gn Window, Code Wi | indow, Server | Explorer, Too | olbox, Dockin | ng Windows, | | | |
| | Properties Explorer, Solution Explorer, Object Browser, Dynamic Help, Task List | | | | | | | | |
| | Explorer | | | | | | | | |
| | Features of .NET, XML Editor, Creating a Project, Add Reference, Build the | | | | | | | | |
| | | Debugging a Project | | 3 | | • | | | |
| Unit 2 | | tion to Standard C | | | | | CO2 | | |
| | Display in | nformation, Accepti | ng user inpu | t, Submitting | form data, | | | | |
| | Displayin | | | _ | | | | | |
| | Using the | panel control, using the | he hyperlink c | ontrol | | | | | |
| | | | | | | | | | |
| | | tion to Validation | | | | | | | |
| | | required field valid | | | | | | | |
| | using the | compare validator of | control, using | g the regular (| expression v | alidator | | | |
| | control, u | sing the custom val | idator contro | l, using the v | alidation sur | nmary | | | |
| | controls. | | | | | | | | |
| | Introduc | ction to Rich Contr | ols | | | | | | |
| | Acceptin | g file uploads, Di | splaying a c | calendar, Dis | playing adv | ertisement, | | | |
| | | ng different page vie | | | | <u> </u> | | | |
| Unit 3 | Designin | g Website with Ma | aster Pages | | | | CO3 | | |
| | Creating | master pages, Mod | lifving maste | er nage conf | ent and | | | | |
| | Creating | master pages, 14100 | injing mast | r page cont | one, and | | | | |

| | Loading master page dynamically. | |
|---------------------|---|-----|
| | List Controls Dropdown list control, Radio button list controls, list box controls, bulleted list controls, custom list controls | |
| | Grid View Controls | |
| | Grid view control fundamentals, Using field with the grid view control, working with grid view control events extending the grid view control. | |
| Unit 4 | SQL Data Source Control | CO4 |
| | Creating database connections, Executing database commands, Using ASP.NET parameters with the SQL data source controls, programmatically executing SQL data source commands. | |
| | Building Data Access Components with ADO.NET | |
| | Connected data access | |
| | Disconnected data access Executing a synchronous database commands, Building database objects with the .NET framework | |
| Practical: | | |
| Text Book/s | | |
| Reference Book/s | 1.Paul J. Deitel and Harvey M. Deitel, C# 2010 for Programmers, Forth Edition New Delhi: Pearson 2010. 2.Imar Spaanjaars, Beginning ASP.NET 4: in C# and VB (Wrox), Paperback Edition, 2010. 3.George Shepherd, Microsoft ASP.NET 4 Step by Step (Microsoft), Paperback Edition, 2010. 4.Scott Mitchell, Teach Yourself ASP.NET 4 in 24 Hours, Complete Starter Kit. | |



| L | T | P | Credit |
|---|---|---|--------|
| 4 | 0 | 0 | 4 |

| Course Code | CSP304 | | | | | | | | | | |
|--------------|---|---|-----------------|---------------|------------------|-----|--------|--|--|--|--|
| Course Title | Cyber Se | curity | | | | | | | | | |
| Course | On the co | On the completion of the course the student will be able to: | | | | | | | | | |
| Outcomes | CO1: Acquire knowledge about various Information Systems. CO2: Understand the key security requirements of Confidentiality, Integrity & Availab CO3: Demonstrate the concept of Intrusion Detection & Intrusion Prevention. | | | | | | | | | | |
| | | | | | | | | | | | |
| | | | | | | | | | | | |
| | CO4: Apply Symmetric Encryption techniques. CO5: Describe the concept of Security policies and Cyber Laws. | | | | | | | | | | |
| | | | | | | | | | | | |
| Examination | Theory | | | | | | | | | | |
| Mode | - | | | | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PB | | | | |
| Tools | Quiz | Project Work | | | | | L | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | | 5% | | | | |
| Syllabus | | | | | | • | CO | | | | |
| | | | | | | | | | | | |
| | | | | | | | g | | | | |
| Unit 1 | Introduct | tion to Cyber securi | ity (12 Hou | rs) | | | CO1 | | | | |
| • | • De | efining Cyberspace | and Overvi | ew of Com | puter | | | | | | |
| | | d Web-technology, | | | | | | | | | |
| | | ommunication and w | | | · | | | | | | |
| | | ternet, World wide | | | ernet. | | | | | | |
| | | ternet infrastructur | , | | , | | | | | | |
| | | vernance, Internet s | | | | | | | | | |
| | _ | egulation of cybe | - | ncept of c | evber | | | | | | |
| | | curity. | | F | . 5 | | | | | | |
| | | sues and challenges | of cyber sec | ıritv. | | | | | | | |
| Unit 2 | | ation of Cybercrim | | | | | CO2 | | | | |
| • | | • | _ | | omputers and | i | | | | | |
| | Common cyber crimes- cybercrime targeting computers and mobiles, cyber crime against women and children. | | | | | | | | | | |
| | Financial frauds, social engineering attacks, malware and | | | | | | | | | | |
| | ransomware attacks, zero day and zero click attacks. | | | | | | | | | | |
| Unit 3 | | me and cyber laws | | | | | CO3 | | | | |
| • | | | odus-operano | di, Repor | ting of | | | | | | |
| | | bercrimes, Remedia | | | \boldsymbol{c} | | | | | | |
| | • | egal perspective of | _ | | | | | | | | |
| | | nendments, Cyber cr | | | | | | | | | |
| | | Organisations dealing with Cybercrime and Cyber | | | | | | | | | |
| | security in India, Case studies. | | | | | | | | | | |
| Unit 4 | | erce and Digital Pa | | Hours) | | | CO4 | | | | |
| | | | ., (, _ | / | | | | | | | |

| Text books | Definition of E- Commerce, Main components of E-Commerce, Elements of E-Commerce security, E-Commerce threats, E-Commerce security best practices. Introduction to digital payments, Components of digital payment and stake holders, Modes of digital payments- Banking Cards, Unified Payment Interface (UPI), e-Wallets, Unstructured Supplementary Service Data (USSD), Aadhar enabled payments. Digital payments related common frauds and preventive measures. Sivanandam, S. N., Sumathi, S., & Deepa, S. N. (2011). Cyber security: Principles and practice. Wiley. Stuttard, D., & Pinto, M. (2011). The web application hacker's handbook. Wiley. Meeuwisse, R. (2017). Cybersecurity for beginners. Independently published. |
|---------------------|--|
| | 4. Howard, R. (2017). <i>The cybersecurity survival guide</i> . Independently published. |
| Reference Book/s | Mishra, R. C. (2010). Cyber crime impact in the new millennium. Author Press. Belapure, S., & Godbole, N. (2011). Cyber security: Understanding cyber crimes, computer forensics, and legal perspectives. Wiley India Pvt. Ltd. Oliver, H. A. (2001). Security in the digital age: Social media security threats and vulnerabilities. Create Space Independent Publishing Platform. Awad, E. M. (n.d.). Electronic commerce. Prentice Hall of India Pvt. Ltd. Kumar, K. (n.d.). Cyber laws: Intellectual property & e-commerce security. Dominant Publishers. |



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 2 | 4 |

| Course Code | CSP307 | | | | | | | | | | |
|--------------|---|--|----------------|---------------|------------|---------|---------|--|--|--|--|
| Course Title | Data Warehousing and Mining | | | | | | | | | | |
| Course | On the completion of the course the student will be able to: | | | | | | | | | | |
| Outcomes | | CO1: Understanding Data Warehousing Concepts. | | | | | | | | | |
| | CO2: Data Modeling and Schema Design. | | | | | | | | | | |
| | CO3: Data Mining Techniques and Algorithms. | | | | | | | | | | |
| | | CO4: Big Data and Advanced Data Mining. CO5: Evaluation and Interpretation of Results. | | | | | | | | | |
| | | | | | | | | | | | |
| Examination | | Theory + Practical | | | | | | | | | |
| Mode | | | | | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | | | |
| Tools | Quiz | Project Work | | | | | | | | | |
| Weightage | 10% | 110ject (oili | 25% | _ | 35% | 25% | 5% | | | | |
| Syllabus | 1070 | | 2570 | | 3370 | 2570 | CO | | | | |
| Synabas | İ | | | | | | Mapping | | | | |
| Unit 1 | Introduc | tion of Data wareh | กาเราก | | | | CO1 | | | | |
| Omt 1 | | | | | | | | | | | |
| | | he need for data wa | _ | | | | | | | | |
| | | perational & Inform | | | | | | | | | |
| | | ata Ware house C | | • | | | | | | | |
| | | Varehouse role & S | Structure, The | e cost of | | | | | | | |
| | | arehousing data | | | | | | | | | |
| | | luction to OLAP | | | | | | | | | |
| | D | ofference between | OLAP & O | LTP. OLAP | Operations | | | | | | |
| Unit 2 | Design ar | nd Implementation | of Data wa | rehouse | | | CO2 | | | | |
| | • Bu | uilding a Data Ware | house | | | | | | | | |
| | • De | esign/Technical/Imp | lementation | Consideratio | ns | | | | | | |
| | | ata Pre-processing | | | | | | | | | |
| | | ımmarization, Data | | | | | | | | | |
| | | ransformation, Cond | _ | | | | | | | | |
| | Overview of Patterns & Models and Artificial Intelligence | | | | | | | | | | |
| | | <u> </u> | | | | | | | | | |
| | Multidimensional Data Model, Schemas for Multidimensional Data (Star Schema, Snowflake Schema, Fact Constellation | | | | | | | | | | |
| Unit 3 | Data Mir | | nownake Sci | icina, raci C | | | CO3 | | | | |
| Omt 3 | Data Mili | iiiig | | | | | CO3 | | | | |
| • | • As | ssociation Rule Mi | ning, Market | Basket Ana | lysis, | | | | | | |
| | | lgorithm, Mining | • | | • | | | | | | |
| | | om Association M | | | | | | | | | |
| | | onstraint Based Ass | | | - | | | | | | |
| | | troduction to Clas | | _ | by decisio | n Tree. | | | | | |
| | | ttribute Selection M | | | , | • | | | | | |
| Unit 4 | | ction to Prediction | | | | | CO4 | | | | |

| • | Accuracy of a Classifier | |
|------------|--|--|
| | Cross-Validation, Bootstrap, Boosting, Bagging | |
| | Introduction to Clustering, Classification of Various Clustering | |
| | Algorithms, Selecting and Using Right DM Technique, Selecting | |
| | and Using Right DM Technique, Data Visualization | |
| Practical: | List of Experiments: | |
| | | |
| | • Task 1: Introduction to Data Warehousing Concepts. | |
| | • Task 2: Dimensional Modelling and Schema Design. | |
| | • Task 3: Data Integration and ETL (Extract, Transform, Load) | |
| | Process. | |
| | Task 4: OLAP Operations and Cube Creation. | |
| | • Task 5: Querying Data from Data Warehouse. | |
| | Task 6: Data Warehouse Data Validation. | |
| | • Task 7: Introduction to Data Marts and their Implementation. | |
| | Task 8: Understand data mining processes, including data cleaning, | |
| | transformation, and mining. | |
| | Task 9: Apply feature selection methods to improve model | |
| | performance. | |
| | Task 10: Analyse the clustering results and evaluate the quality of | |
| | clusters using metrics like silhouette score. | |
| | Task 11: Implement linear regression and logistic regression models. | |
| | Task 11: Implement inical regression and logistic regression models. Task 12: Evaluate and compare different data mining models using | |
| | techniques such as cross-validation, ROC curves, and confusion | |
| | matrix. | |
| | Task 13: Implement anomaly detection techniques on datasets (e.g., | |
| | | |
| Text books | using statistical methods, KNN, or clustering). 1. Kimball, R., & Ross, M. (2013). <i>The data warehouse toolkit: The</i> | |
| Text books | definitive guide to dimensional modeling (3rd ed.). Wiley. | |
| | 2. Ponniah, P. (2010). Data warehousing: Fundamentals for IT | |
| | | |
| | professionals. Wiley. 2. Inmon W. H. (2005). Puilding the data warehouse (4th ed.). | |
| | 3. Inmon, W. H. (2005). Building the data warehouse (4th ed.). | |
| | Wiley. | |
| | 4. Golfarelli, M., Rizzi, S., & Salvi, A. (2009). Data warehouse | |
| | design: Modern principles and methodologies. McGraw-Hill. | |
| Reference | | |
| Book/s | 1. Inmon W. H., <i>Building the Data Warehouse</i> , New York: John Wiley | |
| DOOK S | 2002. | |
| | 2. Inmon W. H., Data Warehousing and Knowledge | |
| | Management, ork: New YJohn Wiley 1996. | |
| | 3. Romez Elmasri, Shamkant B., Navathe, Fundamentals | |
| | of Database Systems, New Delhi:Pearson Education, | |
| | 2009. | |
| | | |
| | 4. Han, Kamber, Morgan Kaufmann, Data Mining: Concepts and | |
| | Techniques, 2nd Edition, Elsevier, 2012. | |

- Inmon, W.H., C. L. Gassey, Managing the Data Warehouse, New York: John Wiley 1999.
 Fayyad, Usama M., Advances in Knowledge Discovery and Data Mining, MIT Press, 1996.
- 7. Silberschatz, Korth and Sudershan, *Database System Concepts*, New Delhi: McGraw Hill, 4th Edition, 2010.

| Course Code | CSP3 | 08 | | | | | | | | | | |
|--------------|--|--|---|----------------------------|-------------|---------------------------|--------------------|---------------|--|--|--|--|
| Course Title | Data Analytics After completion of this course student will be able to early verious concents of data | | | | | | | | | | | |
| Course | After o | fter completion of this course, student will be able to apply various concepts of data | | | | | | | | | | |
| Outcomes | analytics to solve various problems. CO1: Students will be able to articulate meaningful lines of inquiry that might be | | | | | | | | | | | |
| | | | | | | | | | | | | |
| | explo | ored throug | gh the collection, | organizatio | ı, visualiz | zation, and | l analysis of data | in a | | | | |
| | conte | ext associa | ted with their prin | nary field of | f study us | sing (as ap | propriate) numer | ical, | | | | |
| | textu | al, spatial, | and/or visual data | a. | | | | | | | | |
| | | | vill have intermed | | | | | | | | | |
| | | | omputing theory, l | | | | | | | | | |
| | | | models, and the pr | rinciples of | optimiza | tion to ap | propriately formu | ilate | | | | |
| | | ise data an | aryses can use graphs for | · Rig data ar | alytics | | | | | | | |
| | 00+ | . Students | can use graphs for | Dig data ai | iary ties. | | | | | | | |
| Examination | Theory | / Practical | / Theory + Practic | al | | | | | | | | |
| Mode | Theory | Tractical | Theory Tructic | ui | | | | | | | | |
| Assessment | Quiz | Assignm | ABL/PBL | MSE | MTP | ESE | ETP | Total | | | | |
| Tools | | ent/ | | | | | | | | | | |
| | | Project | | | | | | | | | | |
| | | Work | | | | | | | | | | |
| Weightage | 10% | - | 5 | 25% | - | 35% | 25% | 100 | | | | |
| | | | | | | | | ~~ | | | | |
| Syllabus | | | | | | | | CO Manning | | | | |
| UNIT 1 | Introd | luction to | Data Analytics (| 12 Hours) | | | | Mapping | | | | |
| OTVIT I | Introc | idenon to | Data Marytics (| 12 110013) | | | | | | | | |
| | | | alytics - Predictive | | | | | | | | | |
| | linear r | egression | - Auto regression | - Moving | Average - | Autoregr | essive Integrated | CO1 | | | | |
| | Moving | g Average | - Data Pre-proce Data Reduction - | essing - Da Descriptive | ta Cleani | ing - Data lytics - ma | Integration and | | | | | |
| | tendend | v - measu | res of location of | dispersions. | uata ana | iytics - iii | casules of central | | | | | |
| UNIT 2 | | | e Mining (12 Hou | | | | | | | | | |
| | Efficient | d C | labla Europeant Ita | aat Minis | ~ Ma4lea | da Minia | . ~ Various Vinds | | | | | |
| | | | lable Frequent Ite: ules - Association | | | | | | | | | |
| | Based A | Associatio | n Mining - Cluste | r Analysis: | Types of | Data in C | luster Analysis - | | | | | |
| | A Categ | gorization | of Major Clusterin | ng Methods | - Partition | ning Metho | ods - Hierarchical | CO2 | | | | |
| UNIT 3 | method | | Students Concent | ta (12 Hann | <u>a)</u> | | | | | | | |
| UNITS | | | Streams Concepted and architecture | | | - Samplii | ng data in a | | | | | |
| | | | streams - Counting | | | | | | | | | |
| | | | ing oneness in a w | | | | | CO3 | | | | |
| | Analyti | cs Platfori | n (RTAP) applica | tions - case | | | | | | | | |
| TINIT 4 | analysis - stock market predictions. Using Graph Analytics for Big Data (12 Hours) | | | | | | | | | | | |
| UNIT 4 | Ŭ | | • | • | | na triacce | Dolotion using | | | | | |
| | | ung on 1118 Updating | ggers - creation of using | uigger, ins | ertion usi | ng uigger | , Deletion using | | | | | |
| | 115501, | Spanning | | | | | | | | | | |

trigger. Procedures- Creation of Stored Procedures, Execution of Procedure, and Modification of Procedure Cursors- Declaring Cursor, Opening Cursor, Fetching the data, closing the cursor



| L | T | P | Credits | Marks |
|---|---|---|---------|-------|
| 3 | 0 | 2 | 4 | 100 |

| Practical | List of Experiment: - | |
|---------------------|---|--|
| | Task 1: -Perform various data statistical operations on sample dataset. Task 2: -Perform various data pre-processing related operations over dataset using machine learning libraries. Task 3: -Take any sample dataset and demonstrate correlation between features of the dataset using Libraries. Task 4: -Take a sample dataset; reduce number of features applying data dimension reduction technique. Task 5: -Demonstrates K-means data clustering technique using sample dataset. Task 6: -Demonstrates data visualization on sample dataset.8. Apply the analytics and visualization to any real world problem of your choice. Task 7: -Apply statistical tests (e.g., t-tests, chi-square tests) to analyze data distributions. | |
| Textbook | Theobald, O. (2019). Data analytics for beginners: Your step-by-step guide to data analysis (1st ed.). CreateSpace. | |
| Reference Book/s | Zhao, Y. (2020). Hands-on data analysis with R [E-book edition]. Packt Publishing. Han, J., Kamber, M., & Pei, J. (2011). Data mining: Concepts and techniques (3rd ed.). Elsevier. Rajaraman, A., & Ullman, J. (2012). Mining massive data sets. Cambridge University Press. Loshin, D. (2013). Big data analytics: From strategic planning to enterprise integration with tools, techniques, NoSQL, and graph. Morgan Kaufmann. | |



| L | T | P | Credits | Marks |
|---|---|---|---------|-------|
| 3 | 0 | 2 | 4 | 100 |

Page | 52 Batch: 2025

| Course Code | CSP309 | | | | | | | | | | |
|--|---|---------------------------------|------------------------------|---|--------------------------------------|---------------------------|---|-------------------------------|---------------|--|--|
| Course Title | Big Data | 6 | | | | | | | | | |
| Course Outcomes | After completion of this course, student will be able to apply various concepts of dat analytics to solve various problems. CO1: Understand the concepts of distributed file system. CO2: Learn abstraction of Hadoop environment. CO3: Study the Hadoop architecture. CO4: Know the Hadoop ecosystem and yarn components. Learn different architecture like HIVE and HIVEQL, HBASE. | | | | | | | | | | |
| Examination Mode | Theory + | Practica | 1 | | | , | | | | | |
| Assessment Tools | Quiz | Assign: Project Work | | ABL/PBL | MSE | MSP | ESE | ESP | Total | | |
| Weightage | 10 | - | | 5 | 25 | - | 35 | 25 | 100 | | |
| Syllabus | | | | | | | | | CO Mapping | | |
| UNIT 1 | Introdu | ction to | Big D | ata (12 Hou | ırs) | | | | | | |
| | Systems, Analysis Sampling | Intellige vs. Re Distribu | ent dat porting ations | a analysis, g, Modern - Re-Sampli | Nature of Data Ar ing, Statist | Data, Ananalytic To | Challenges of alytic Processools, Statistic ence - Prediction | es and Tools, al Concepts: | | | |
| UNIT 2 | Mining | Data St | reams | :(12 Hours |) | | | | | | |
| | Computing Elements | ng, Sam in a St | pling ream, | Data in a Estimating | Stream, F Moments | filtering S s, Countir | el and Archite treams, Coun ng Oneness in P) Application | nting Distinct n a Window, | CO2 | | |
| UNIT 3 | Hadoop | : (12 Ho | urs) | | | | | | | | |
| | History of Hadoop, The Hadoop Distributed File System, Components of Hadoop, Analyzing the Data with Hadoop, Scaling Out- Hadoop Streaming, Design of HDFS-Java interfaces to HDFSBasics, Developing a Map Reduce Application, How Map Reduce Works, Anatomy of a Map Reduce Job run-Failures, Job Scheduling-Shuffle and Sort, Task execution, Map Reduce Types and Formats, Map Reduce Features | | | | | | CO3 | | | | |
| UNIT 4 | Framew | orks:(12 | Hours | s) | | | | | | | |
| UNIT 4 Frameworks:(12 Hours) Applications on Big Data Using Pig and H PigHive services, HiveQL, Querying Data Zookeeper, Visualizations: Visual data ana Systems and applications | | | | | g Data in | Hive, Fun | damentals of I | HBase and | CO4 | | |

| Practical | List of Experiment: - | |
|------------|--|--|
| | Task 1: - Big Data Overview and Exploration Task 2: - Stram Data Simulation and Stream Data Architecture Design Task 3: -Hadoop Installation and Setup Task 4: - Install and configure Apache Pig. Task 5: - Set up HBase and Zookeeper on a local machine or a Hadoop cluster. Task 6: -Demonstrates data visualization on sample dataset.8. Apply the analytics and visualization to any real-world problem of your choice. Task 7: -Apply statistical tests (e.g., t-tests, chi-square tests) to analyze data distributions. | |
| Textbook | Chandmouli, S. (2021). Big data analytics. S. Chand Publishing. | |
| References | Chen, M. (2017). Big Data: Challenges and opportunities. Springer. Davenport, T. H., & Bean, R. (2018). Big data at work: Dispelling the myths, uncovering the opportunities. Harvard Business Review Press. Michael Berthold, David J. Hand, Intelligent Data Analysis, Springer, 2007. Chris Eaton, Dirk DeRoos, Tom Deutsch, George Lapis, Paul Zikopoulos, Understanding Big Data: Analytics for Enterprise Class Hadoop and Streaming Data, 2012. Tom White, Hadoop: The Definitive Guide Third Edition, O'reilly Media, 2012. AnandRajaraman and Jeffrey David Ullman, Mining of Massive Datasets, Cambridge University Press, 2012. Bill Franks, Taming the Big Data Tidal Wave: Finding Opportunities in Huge Data Streams with Advanced Analytics, JohnWiley& sons, 2012. Michael Minelli (Author), Michele Chambers (Author), AmbigaDhiraj (Author), Big Data, Big Analytics: Emerging Business Intelligence and Analytic Trends for Today's Businesses, Wiley Publications, 2013. Jiawei Han, MichelineKamber, Data Mining Concepts and Techniques, Second Edition, Elsevier, Reprinted 2008. | |

SEMESTER 6



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 0 | 3 |

| Course | CSP3 | 310 | | | | | | |
|--------------|---|---|----------------|--------------|--------------|----------------|---------------------------|--------------|
| Code | | | | | | | | |
| Course Title | Design | n and Analysis | of Algorith | ım | | | | |
| Course | After c | completion of this | s course, stud | dent will be | able to ap | oly various co | ncepts of dat | a |
| Outcomes | | cs to solve variou | | | | | | |
| | | Define and under | | - | lated to alg | gorithms, incl | luding algorit | hm |
| | | analysis, and the | | | | | | |
| | | analyze the time | and space co | omplexity of | of algorithn | ns using Big- | O , Big- Ω , and | d |
| | _ | notations. | | | 1 1. | 1 2.1 | 1 1 . | |
| | | Design and analy | yze commoi | n sorting a | na searchii | ng algorithms | s and evaluat | e |
| | their performance. CO4: Design and implement dynamic programming algorithms to solve problems | | | | | | | .0 |
| | with optimal substructure. | | | | | | | 1.5 |
| Examination | | | 16. | | | | | |
| Mode | lincory | | | | | | | |
| Assessment | Quiz | Assignment/ | ABL/PBL | MSE | MSP | ESE | ESP | Total |
| Tools | Quiz | Project Work | TIDE, T DE | MOL | 1,101 | LSL | LSI | 1000 |
| | | Troject Work | | | | | | |
| | | | | | | | | |
| Weightage | 10 | 10 | 5 | 25 | - | 50 | - | 100 |
| Syllabus | | | | | | | | CO Mappin |
| UNIT 1 | Introd | duction (12 Hou | rs) | | | | | g |
| | Concept of Algorithm, Role of Algorithms in Computing, Algorithm Specification, Performance Analysis (Time and space complexities), and Growth of functions: Asymptotic Notation, Standard notation & Description for Recurrences: substitution method, recursion-tree method, master method, Brute-Force, Branch and Bound, Randomizing Algorithms, Depth First Search (DFS) and Breadth First Search (BFS), Topological sorting. Divide and Conquer, General Method, Binary Search, Merge sort, Quick sort, Selection sort. | | | | | | COI | |
| UNIT 2 | Greed | ly Algorithms:(| 11 Hours) | | | | | |
| | Elements of Greedy strategy, Activity Selection Problem, Knapsack problem, Minimum Cost Spanning Trees (Prim's Algorithm, Kruskal's Algorithm), Single source Shortest paths problem and analysis of these problems. | | | | | | CO2 | |
| UNIT 3 | Dynar | nic Programmiı | ng: (11 Hou | rs) | | | | |
| | Elemer chain n | Dynamic Programming: (11 Hours) Clements of dynamic programming, Assembly-line scheduling problem, Matrixhain multiplication, Multistage Graph, All Pairs Shortest paths, Longest common absequence, Bin Packing, 0/1 Knap Sack and Travelling Salesman Problem. | | | | | | |

| UNIT 4 | Back Tracking:(11 Hours) | |
|--------|--|-----|
| | General method, 8 queen's problem, Graph coloring and Hamiltonian Cycles, 0/1 Knap Sack Problem, NP-Completeness, Polynomial Time, polynomial-time verification, NP completeness & reducibility, NP-complete problems, Cook's theorem, Approximation algorithms. | CO4 |

| Textbook | Levitin, A. (2012). <i>Introduction to the design and analysis of algorithms</i> (3rd ed.). Pearson. | |
|------------|---|--|
| References | Cormen, T. H., Leiserson, C. E., Rivest, R. L., & Stein, C. (2020). Introduction to algorithms (3rd ed.). MIT Press. Kleinberg, J., & Tardos, É. (2006). Algorithm design. Pearson Education. Backhouse, R. (2015). Algorithmic problem solving. Springer. Cormen, T. H. (2013). Algorithms unlocked. MIT Press. | |



| L | T | P | Credit |
|---|---|---|--------|
| 3 | 0 | 0 | 3 |

| Course Code | CSP311 | | | | | | |
|--------------|--|-----------------------|---------------|----------------|-----------------|-------------|-----------|
| Course Title | Artificial | Intelligence | | | | | |
| Course | CO1: Demonstrate fundamental understanding of the history of artificial intelligence | | | | | | |
| Outcomes | | its foundations. | | J | · | | <u> </u> |
| | CO2: Apply basic principles of AI in solutions that require problem solving, | | | | | | |
| | inference, Perception, knowledge representation, and learning. | | | | | | |
| | CO3: Demonstrate awareness and a fundamental understanding of various applications | | | | | | |
| | of I tech | niques in intelligent | agents, expe | ert systems, a | ırtificial neuı | al networks | and other |
| | | learning models. | | | | | |
| | | monstrate proficience | | g application | ns in an 'AI | language', | expert |
| | _ | nell, or data mining | tool. | | | | |
| Examination | Theory + | Practical | | | | | |
| Mode | | | . | ı | ľ | | T |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL |
| Tools | Quiz | Project Work | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | |
| Syllabus | | | | | | | CO |
| | | | | | | | Mapping |
| Unit 1 | Introduc | ction | | | | | CO1 |
| | Backgrou | and History | | | | | |
| | Overview | of AI applications | Areas | | | | |
| | Knowled | ge Representation | | | | | |
| | | Representation-Ass | | work & | | | |
| | Conceptu | | | | | | |
| | Structure | d Representation- F | rames & Scri | pts | | | |
| Unit 2 | | Strategies | | • | | | CO2 |
| | | For State Space Se | arch-Data D | riven And G | loal | | |
| | Driven Se | | | | | | |
| | Search A | lgorithms- Uninfor | med Search | (Depth First | ţ , | | |
| | | First, Depth First V | | | | | |
| | Informed | Search (Hill Climb | ing, Best Fir | st, A* Algor | rithm, | | |
| | etc) | | | | | | |
| | Expert S | ystems | | | | | |
| | | ion, Examples | | | | | |
| | Character | ristics Architecture, | People Invol | ved and Thei | r Role in Bu | ilding an | |
| | Expert Sy | | | | | | |
| Unit 3 | Natural l | Language Processi | ng | | | | CO3 |
| | Introduct | ion to Natural Lang | uage Process | ing —— | | | |
| | Compone | ent Steps of Commu | nication | | | | |
| | Contrast 1 | Between Formal and | d Natural Lai | nguages in th | e Context of | Grammar | |
| | | ction to AI langua | | | | | |
| | Introduct | tion to LISP and Pro | olog | | | | |

| Unit 4 | Planning | CO4 |
|-------------------|---|-----|
| | Basic Representation for Planning | |
| | Symbolic-Centralized Vs. Reactive-Distributed | |
| | | |
| | Pattern Recognition | |
| | Introduction | |
| | Recognition & Classification Process | |
| | Learning classification patterns and clustering | |
| Practical: | | |
| Text Book/s | | |
| Reference | | |
| Book/s | 1. Elaine Rich, Kevin Knight and Nair Shiva Shankar | |
| | B, Artificial Intelligence, Third Edition, New Delhi: Tata- | |
| | McGraw Hill, 2008. | |
| | 2. Winston, P.H. and Horn, B.K.P, <i>LISP</i> , Pearson, 1993. | |
| | 3. Rajasekharan, S. and VijayalakshmiPai, G. A., Neural | |
| | Networks, Fuzzy Logic and Genetic Algorithms, New | |
| | Delhi: Prentice Hall of India, 2003. | |
| | 4. Luger George F., Artificial Intelligence, 5 th edition, Pearson | |
| | Education. | |
| | 5. Patterson Dan W., Introduction to Artificial Intelligence | |
| | and Expert syste, New Delhi: PHI, 2005. | |
| | Bharti & Chaitany, Natural Language Processing, New Delhi: PHI, 2006 | |



| L | T | P | Credit | | |
|---|---|---|--------|--|--|
| 3 | 0 | 0 | 3 | | |

| Course Code | CSP312 | | | | | | | | |
|--------------|---|---|--------------------------------|------------------------|-------------------|------------|----------------|--|--|
| Course Title | Software | Engineering | | | | | | | |
| Course | CO1: Decompose the given project in various phases of a lifecycle. Choose appropriate | | | | | | | | |
| Outcomes | | nodel depending on | | | • | | 11 1 | | |
| | CO2: Per | form various life c | ycle activition | es like analy | sis, design, im | plementa | ation, testing | | |
| | and maint | tenance. Recognize | various proc | esses used in | all the phases of | of the pro | oduct. | | |
| | CO3: App | CO3: Apply the knowledge, techniques, and skills in the development of a software | | | | | | | |
| | product. | | | | | | | | |
| | CO4: Explain project management techniques. | | | | | | | | |
| Examination | Theory + Practical | | | | | | | | |
| Mode | | | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | |
| Tools | Quiz | Project Work | | | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | | | |
| Syllabus | | | | | | | CO | | |
| | | | | | | | Mapping | | |
| Unit 1 | Software | e engineering Back | ground | | | | CO1 | | |
| | Introducti | on to Software Eng | ineering, Sof | tware engine | ering principles | } | | | |
| | How is so | oftware engineering | an engineeri | ng discipline | | | | | |
| | Information | on system characte | eristics, soft | ware develo | pment | | | | |
| | process m | nodels, | | | | | | | |
| | Life Cycle Concepts, Software Phases and Deliverables, Software | | | | | | | | |
| | Developm | nent Strategies | | | | | | | |
| Unit 2 | Technica | al development | | | | | CO2 | | |
| | Structured | d systems analysis a | nd design re | quirements | | | | | |
| | | n and Specification | C | 1 | | | | | |
| | | bjectives, Design Pr | rinciples | | | | | | |
| | _ | v and Logical Data | - | ost Benefit A | nalysis | | | | |
| | | y study, User Interfa | _ | | - | | | | |
| | | Development Strate | | • | _ | | | | |
| | | d Programming | gies. Top u | own und Be | ttom up, | | | | |
| | | Level of testing, T | est cases an | d test criteri | a | | | | |
| | _ | al Testing, Structura | | a test circii | α, | | | | |
| Unit 3 | | e project managem | | | | | CO3 | | |
| | | s of software project | | ent organizat | tional and | | | | |
| | team struc | 1 3 | | 0104111241 | | | | | |
| | Project Planning, Project Initiation and Project Termination; Technical | | | | | | | | |
| · I | Project Pl | anning, Project Initi | iation and Pr | oiect Termin | ation: Technica | 1 | | | |
| | • | | | • | | 1 | | | |
| | Quality A | And Management P | lans, Project | t Controls, C | Cost | l | | | |
| | Quality A Estimation | And Management P n Methods-Function | Plans, Project n Points and | t Controls, CCOCOMO, 7 | Cost Гools | | | | |
| | Quality A Estimation | And Management P n Methods-Function quality manageme | Plans, Project n Points and | t Controls, CCOCOMO, 7 | Cost Гools | | | | |

| | Software metrics, verification and validation Software configuration management. Formal, semi-formal and informal methods Data function, and event based modeling CASE Tools, CASE Standards Software documentation, Types of software Maintenance | |
|-------------|--|--|
| Practical: | Software Goodship Lights of Software Frankling | |
| Text Book/s | | |
| Reference | 1. Pressman R. S., Software Engineering: A | |
| Book/s | practitioner's Approach, New York: McGraw | |
| | Hill, Seventh Edition 2010. | |
| | 2. Jalote Pankaj, An Integrated Approach to Software | |
| | Engineering, New Delhi:Pearson 2010. | |
| | 3. Sommerville I., <i>Software Engineering</i> , Addison –Pearson, Eighth | |
| | Edition 2009 | |



| L | T | P | Credit | | |
|---|---|---|--------|--|--|
| 3 | 0 | 0 | 3 | | |

| Course Code | CSP313 | | | | | | | | |
|--------------|--|--|--------------|----------------|-------------|-------------|---------|--|--|
| Course Title | Mobile Application Development | | | | | | | | |
| Course | | ompletion of the cou | | nt will be abl | e to: | | | | |
| Outcomes | CO1: Discuss android history, versions with its characteristics and application model. | | | | | | | | |
| o dicomes | CO2: Describe UI Widgets and Activity, Intent and Fragment. | | | | | | | | |
| | | CO3: Introduce android Menu and Layout Manager and Android Service | | | | | | | |
| | | CO4: Learn content provider fundamentals and multimedia. | | | | | | | |
| Examination | Theory+Practical | | | | | | | | |
| Mode | | 10001001 | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | |
| Tools | Quiz | Project Work | | | -~- | | | | |
| Weightage | 10% | - | 25% | _ | 35% | 25% | 5% | | |
| Syllabus | 1070 | | 1 20 70 | | 100,0 | 2070 | CO | | |
| | | | | | | | Mapping | | |
| Unit 1 | Introduc | tion and its Signific | cance (10 Ho | ours) | | | CO 1 | | |
| • | | Android, History of | | | Java Lanoi | nage for | | | |
| | | apps, other mobil | | _ | _ | _ | | | |
| | developm | | C OB Cs, 1 | marora vers | sions and | different | | | |
| • | | | Framework | e and Tools | Application | n Model | | | |
| | | Characteristics and Benefits, Frameworks and Tools, Application Model Profiles of Mobile Devices | | | | | | | |
| | 1 TOTHES OF WICHIE DEVICES | | | | | | | | |
| Unit 2 | UI Widge | UI Widgets and Activity, Intent & Fragment (12 Hours) | | | | | | | |
| • | | Working with Button, Toast, CustomToast, Button, Toggle Button, | | | | | | | |
| | _ | itton, Image Button, | | Toust, Dutte | 711, 105510 | Batton, | | | |
| • | | alog, Spinner, Auto | | extView. Rat | ingBar. Dat | ePicker. | | | |
| | | ter, ProgressBar, Qu | - | | 82, 2 | ,01 101101, | | | |
| | | , 11081088241, Qu | | 2 0.080 | | | | | |
| Unit 3 | Android | Menu and Layout | Manager (1 | 2 Hours) | | | CO 3 | | |
| • | | Ienu, Context Menu | | | ative Layou | t, Linear | | | |
| | - | Table Layout and Gri | | | Ĭ | , | | | |
| | Android | | • | | | | | | |
| • | Android | Service, Android Se | rvice API, A | Android Start | ed Service, | Android | | | |
| | | ervice, Android Serv | | | | | | | |
| | | <u> </u> | <i>J</i> - | | | | | | |
| Unit 4 | Content | Provider and Multi | imedia (12 I | Hours) | | | CO 4 | | |
| • | | Provider Fundamen | • | | reating Not | ification | | | |
| | | Playing Audio | , 1,0011100 | | 1.00 | | | | |
| • | | API, Working with | Camera, Mo | tion Sensor | | | | | |
| • | | P2P Communication | | | .p | | | | |
| Practical | | xperiments: | | | 1 | | | | |
| | | mulator to deploy a | nd run mobil | e apps | | | | | |
| | _ | an Android applicat | | | ame of the | user and | | | |
| <u> </u> | 2.010 Whit interests upper which the short of the dest which | | | | | | | | |

| | run it on an emulator. | |
|-----------|---|--|
| | 3.Create an application that takes the name from a text box and shows hello | |
| | message along with the name entered in text box, when the user clicks the | |
| | OK button. | |
| | 4.Develop an ANDRIOD application that uses GUI components, Font and | |
| | Colors. | |
| | 5. Write an application that draws basic graphical primitives on the screen. | |
| | Develop an application that uses Layout Managers and event listeners. | |
| | 6. Create and Login application as above. On successful login, open browser | |
| | with any URL. | |
| | 7. Testing mobile app - unit testing, black box testing and test automation. | |
| Textbook | 1. Anubhav Pradhan, Anil V Deshpande, "Mobile Apps Development" | |
| | Edition: I | |
| | 2. Jeff McWherter, Scott Gowell "Professional Mobile Application | |
| | Development", John Wiley & Sons, 2012. | |
| Reference | 1.Os Swift, "Android App Development & Programming Guide: Learn in a Day", | |
| Book/s | CreateSpace Independent Publishing Platform (October 2, 2015). | |
| | 2. David Griffiths and Dawn Griffiths, "Head First Android Development: A Brain | |
| | Friendly Guide", Shroff (1 January 2015). | |
| | 3. Ted Hagos "Learn Android Studio 3 with Kotlin: Efficient Android App | |
| | Development", Apress media LLC, Newyork, 2018 | |
| | 4. Zigurd Mednieks, G. Blake Meike, Laird Dornin, Masumi Nakamura, | |
| | "Programming Android: Java Programming for the New Generation of Mobile | |
| | Devices", 2nd Edition, Kindle Edition, O'Reilly Media; 2 edition (28 September | |
| | 2012). | |
| | | |



| L | T | P | Credit | Marks | | |
|---|---|---|--------|-------|--|--|
| 3 | 0 | 0 | 3 | 100 | | |

| Course Code | CSP314 | | | | | | | | |
|--------------|--|---|-----------------|-----------------|------------------|---------------|---------------|--|--|
| Course Title | Discrete | Mathematics | | | | | | | |
| Course | CO1: To | acquaint the student | s with the ba | sic concepts | of set, relation | on and func | tion. | | |
| Outcomes | CO2 : To | acquaint the stud | lents with tl | ne basic con | ncepts of P | igeonhole j | principle | | |
| | and permi | utation and combina | ition. | | | | | | |
| | CO3: To | acquaint the studen | ts with the ba | asic concepts | of recursive | e relation ar | nd generating | | |
| | functions. | | | | | | | | |
| | | acquaint the student | | | 0 1 | | | | |
| | | acquaint the student | s with the ba | sic concepts | of Inference | theory. | | | |
| Examination | Theory + Practical | | | | | | | | |
| Mode | | 1 | | | | | | | |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | |
| Tools | Quiz | Project Work | | | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | | | |
| Syllabus | | | | | | | CO | | |
| | | | | | | | Mapping | | |
| Unit 1 | Introduc | tion | | | | | CO1 | | |
| | Introducti | on to Sets | | | | | | | |
| | | Infinite Sets, Unac | countably In | finite Sets | | | | | |
| | | on to Functions a | • | | of Rinary | relations | | | |
| | | Partial Ordering Rel | | s, Troperties | of Dinary | iciations, | | | |
| Unit 2 | | le Principle | ations. | | | | CO2 | | |
| | | on and Combination | ns Mathema | tical Induction | าท | | 202 | | |
| | | of Inclusion and Ex | | iicai iiiaacii | 311, | | | | |
| | _ | ic Notations | | | | | | | |
| Unit 3 | | nce Relations | | | | | CO3 | | |
| Omt 5 | | on, Generating Fun | nctions I ine | ear Recurren | ce | | CO3 | | |
| | | with constant coeff | | | cc | | | | |
| | Graphs' | | icicinis una ti | ien solution | | | | | |
| | _ | erminology of G | raphs. Mod | els and T | vpes. | | | | |
| | | ohs, Weighted Gra | | | | | | | |
| | | somorphism Graph | | | | | | | |
| | _ | ian Paths and Circ | | - | | | | | |
| | | Basic Terminolog | | - | 1 | | | | |
| | Propertie | s of Trees, Spannin | g Trees. | | | | | | |
| Unit 4 | Inferenc | | - | | | | CO4 | | |
| | Introducti | on, LogicalConne | ctives, Well | Formed | Formul | las, | | | |
| | Tautologies, Equivalence | | | | | | | | |
| Practical: | | | | | | | | | |
| Text Book/s | | | | | | | | | |
| Reference | | | | | | | | | |
| Book/s | 1. C. L. Liu and D.P. Mohapatra, <i>Elements of Discrete</i> | | | | | | | | |
| | l I | Mathematics, Third Edition, Tata McGraw Hill, | | | | | | | |

2008.

- 2. K. Rosen, *Discrete Mathematics and Its Applications*, Sixth Edition, Tata McGraw Hill,2007.
- 3. T.H. Cormen, C.E. Leiserson, R.L. Rivest, *Introduction to Algorithms*, Third Edition, Prentice Hall of India, 2010.
- 4. J.P. Trembley, R. Manohar, *Discrete Mathematical Structures with Application to Computer Science*, First Edition, Tata McGraw Hill, 2001.
- 5. David Gries, Fred B. Schneider, *A Logical Approach to Discrete Math*, Springer; 2010.

SEMESTER 7



| L | T | P | Credits | | | |
|---|---|---|---------|--|--|--|
| 4 | 0 | 0 | 4 | | | |

| Course Code | CSP405 | | | | | | | |
|--------------------|--|--|--------------|-------------|------------|---------------|-----------|--|
| Course Title | Theory o | f Computer So | cience | | | | | |
| Course Outcomes | | mpletion of the | | e student | will be ab | le to: | | |
| | | derstanding of | | | | | automata | |
| | | h minimization | | | • • • | | | |
| | | ility to develop | | | for variou | us regular la | anguages | |
| | CO 3: Un | CO 3: Understanding of context free language and grammar, ambiguity in | | | | | | |
| | | grammar and simplification of context free grammar. | | | | | | |
| | CO 4: Un | derstanding of | push dow | n automat | a and abil | ity to devel | lop the | |
| | push dow | n automata for | various co | ontext free | e language | es | | |
| | CO 5: Un | derstanding of | Linear bo | und auton | nata and a | bility to de | velop the | |
| | Turing ma | achine for vario | ous linear | bound aut | omata. Ur | nderstandin | g of | |
| | Halting a | nd Undesirabili | ty of prob | lem and the | he Choms | ky hierarch | ıy | |
| Examination | Theory | | | | | | | |
| Mode | | T | | r | | 1 | | |
| Assessment Tools | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | |
| | Quiz | Project | | | | | | |
| | | Work | | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | 5% | |
| Syllabus | | | | | | | CO | |
| | | | | | | | Mapping | |
| Unit 1 | | a Theory (15) | | | | | CO1 | |
| | | istic Finite Aut | | | | | | |
| | 1 | rministic Finite | | a. | | | | |
| | | d Mealy Machi | | | | | | |
| | | tion Algorithm | | | | | | |
| Unit 2 | | Free Grammai | • | | | | CO2 | |
| | | ree grammars (| | | raphs | | | |
| | | ies in Grammaı | | | 1.0 | - · | | |
| | _ | s of Context Fre | _ | - | nal Forms | , Pumping | | |
| | | or CFL, Closure | e Propertion | es | | | | |
| | | n Automaton | DA) D | ,. | D 1.1 | | | |
| | | n Automaton (F | | | | | | |
| | | on (DPDA), No | - | ence of Pi | JA and D | PDA, | | |
| Unit 2 | | Accepted by P | | \) (15 IIa | | | CO2 | |
| Unit 3 | | ounded Auton | | | ours) | | CO3 | |
| | | LBA, Closure j Iachines: Turir | | | odal of | | | |
| | | | ig iviaciiii | ic as A IVI | ouel ol | | | |
| | Computation Programming with a Turing Machine, Varients of Turing | | | | | | | |
| | _ | Programming with a Turing Machine, Variants of Turing | | | | | | |
| | Machine and Their Equivalence, Turing Machines and | | | | | | | |
| Unit 4 | Languages Undecidability (15 Hours) CO4 | | | | | | CO4 | |
| Unit 4 | | Hierarchy of | | PC | | | CO4 | |
| | Chomsky | Therarchy of | Languag | CS | | | | |

| | Recursive and Recursive-Enumerable Languages |
|-----------|--|
| | Halting Problem, Undecidable Problems about Turing |
| | machines |
| | Rice theorem |
| | The Equivalence of the Automata and the Appropriate |
| | Grammars |
| Reference | 1. G.E. Reevsz, Introduction to Formal Languages, New Delhi: McGraw |
| Book/s | Hill 1983. |
| | 2. Lewis H.R., Papadimitriou C.H., Elements of the Theory of |
| | Computation (2nd ed.), NJ:Prentice-Hall,1997. |
| | 3. Anderson J.A., Automata Theory with Modern Applications, New |
| | York: Cambridge UniversityPress, 2006. |
| | 4. Lewis, Harry R. and Papadimitriou, Christos H.: Theory of |
| | Computation, Prentice Hall of India, 1996. |
| | 5. Hopcroft, John E. and Ullman, Jefrey D.: Introduction to Automata |
| | Theory, Languages and Computation, Addison-Wesley Publishing |
| | Company Inc |
| | 6. Brady, J.M.: Theory of Computer Science, Wiley. |
| | 7. Dewire, Dawna Tranis: Client Server Computing, McGraw Hill. |
| | 8. Aho,Lam,Sethi and Ullman: Compilers Principles, Techniques and |
| | Tools, Publisher Pearson. |
| | |



| L | T | P | Credits |
|---|---|---|---------|
| 4 | 0 | 0 | 4 |

| Course | CSP401 | | | | | | |
|----------------------|--|---------------------------------|-------------|--------------|--------------|-------------|---------------|
| Code Course Title | Dogoonah | Mothodology | | | | | |
| Course | | Methodology npletion of the co | ource the c | tudent wil | l he able to | · · | |
| Outcomes | | dent must be able | | | | | nods of |
| Outcomes | | Scope of research | | | | uren, men | 1045 01 |
| | CO 2 Stud | ion & | | | | | |
| | | n Analysis. | | | J | δ | |
| | CO 3: Crea | ating dynamic we | ebsites wit | h help of r | e-usable c | compon St | udent must be |
| | | lerstand Hypothe | U | | | | |
| | | dent must be know | wing disse | ertation des | sign and re | eport writi | ng. |
| Examination Mode | Theory+ P | ractical. | | | | | _ |
| Assessment | Written | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL |
| Tools | Quiz | Project Work | | | | | |
| Weightage | 10% | 10% | 25% | - | 50% | - | 5% |
| Syllabus | | | | | | | CO Mapping |
| Unit 1 | | | (10 Hour | | | | CO1 |
| | | Research: Nature | • | | | | |
| | Methods o | | | | | | |
| | Motivation | | | | | | |
| | problem. | :4 | | | | | |
| | Scope of repreparation | | | | | | |
| | Reviewing | | | | | | |
| | between us | | | | | | |
| | searching i | | | | | | |
| | relevant w | | | | | | |
| Unit 2 | | CO2 | | | | | |
| | Statistical Analysis: Introduction to statistical analysis: Measures | | | | | | |
| | of central t | | | | | | |
| | mean devia | | | | | | |
| | Regression | | | | | | |
| Unit 3 | Probability | Distribution | (12 House | •a) | | | CO3 |
| Unit 3 | Test of Hy | pothesis: Test of | (12 Hour | | deas of tes | ting of | CO3 |
| | | ; Tests of signific | | | | | |
| | • • | ributions. Analys | | | | | |
| | Experimen | | | | | | |
| | and randor | | | | | | |
| Unit 4 | (11 Hours) | | | | | | CO4 |
| | | on to dissertation | | | | _ | |
| | | on: Tabular and g | - | - | | llts, | |
| | | references and p | | | | 1 | |
| | Plagiarism: Introduction, types of plagiarism, plagiarism detection tools. | | | | | | |

| References | 1.Hogg, R.V. & Craig, A. T, Introduction to Mathematical Statistics, MacMillan, |
|------------|---|
| Book/s | 1965. |
| | 2.Goon, A. M., Gupta, M. K. & Dasgupta, Fundamentals of Statistics, Vol. I, World Press, 1975. |
| | 3. Gupta, S.C. & Kapoor, V. K, Fundamentals of Mathematical Statistics, Sultan Chand & Sons, 1994. |
| | 4.Dowdy, S., Wearden, S. and Chilko, D., Statistics for Research, Wiley Series (2004) |
| | 5. Walpole, R.E., Myers, R.H., Myers, S.L. and Ye, K., Probability and Statistics for Engineers and Scientists, Pearson Education (2002). |
| | 6.Borth, Wayne C, et. Al. The Craft of Research Chicago Guides to Writing Edition and Publishing. |
| | 7.Johnson, R.A., Probability and Statistics, PHI, New Delhi, 1994. |
| | 8.Meyer, P. L, Introduction to Probability & Statistical Applications, Oxford, IBH, 1986. |
| | IBH, 1700. |
| | |



| L | T P | | Credits | | |
|---|-----|---|---------|--|--|
| 3 | 0 | 2 | 4 | | |

| Course | CSP404 |
|--------|---------------------------|
| Code | |
| Course | Advanced Operating System |

| Title | | | | | | | | |
|---------------------|--|--------------------------------|---------|----------|---------|-------------------|----------|------------|
| Course Outcomes | 1 | | | | | | | hines. |
| Examination Mode | Theory+ | Practical. | | | | | | |
| Assessment Tools | Written Quiz | Assignment/ Project Work | MSE | MTP | ESE | EPR | | ABL/PBL |
| Weightage | 10% | 10% | 25% | - | 50% | - | | 5% |
| Syllabus | | | | | | | | CO Mapping |
| Unit 1 | Basics of | f Operating S | ystems | (13 Ho | ours) | | | CO1 |
| | 0 | G , G, | . 0 | | 1. | g : | | |
| | | g System Struc | | | | | G . | |
| | • | Calls, Operating | g-Syste | m Desig | gn and | Implementation | ; System | |
| | Boot. Process Management: Process Scheduling and Operations | | | | | | | |
| | Process Management: Process Scheduling and Operations. Inter process Communication, Communication in Client–Server | | | | | | | |
| | - | | | | | | | |
| | Systems, Process Synchronization, Critical-Section Problem, Peterson's Solution, Semaphores, Synchronization. | | | | | | | |
| | Threads: Multicore Programming, Multithreading Models, Thread | | | | | | | |
| | Libraries, Implicit Threading, Threading Issues. | | | | | | | |
| | CPU Scl | | | | | | | |
| | | Scheduling, Mu | | | | duling, Real-Tir | ne CPU | |
| | | | | | | | | |
| | Deadlocks: Deadlock Characterization, Methods for Handling Deadlocks, Deadlock Prevention, Avoidance and Detection; Recovery from Deadlock. | | | | | | | |
| Unit 2 | Memory Management (13 Hours) | | | | | | | CO2 |
| Omt 2 | Contiguous Memory Allocation, Swapping, Paging, Segmentation. | | | | | | | CO2 |
| | Demand | | Replace | | | on of Frames, T | | |
| | Storage 1 | Management: N | Mass-St | orage S | tructur | e, Disk Structur | e, | |
| | _ | ng and Manage | | _ | | | | |
| Unit 3 | | Input /Outpu | | | | | | CO3 |
| | | Methods, Direct | | | | | | |
| | | | File Sh | aring, F | ile-Sys | stem Structure a | nd | |
| | Impleme | | | | | | | |
| | • | y Implementati | | | | ds, Free-Space | | |
| | | ment, Efficienc | - | | | C T7 1.7 | /0 | |
| | - | | | | | erface, Kernel L | | |
| | | | | | | ardware Operation | | |
| | • | | | | | Control, Revoca | | |
| | Access Rights, Program Threats, System and Network Threats Cryptography as a Security Tool, User Authentication, Implementing | | | | | | | |
| | Cryptogi | | | | | | | |

| L | T | P | Credits |
|---|---|---|---------|
| 3 | 0 | 2 | 4 |

| | Security Defences. | , |
|-----------|--|------------------|
| | Virtual Machines: Types of Virtual Machines and Implementations; | |
| | Virtualization. | |
| Unit 4 | Linux Operating Systems (10 Hours) | CO4 |
| | Design Principles, Kernel Modules, Process Management, Scheduling, | |
| | Memory Management, File Systems, Input and Output; Inter process | |
| | Communication, Network Structure. | |
| | Windows Operating Systems: Design Principles, System Components, | |
| | Terminal Services and Fast User Switching; File System, Networking | |
| | Distributed Systems: Types of Networks based Operating Systems, | |
| | Network Structure, Communication Structure and Protocols; Robustness, | |
| | Design Issues, Distributed File Systems. | |
| | 1. Operating System Principles, Abraham Silberchatz, Peter B.Galvin, Gr. | eg Gagne,8th |
| Reference | Edition, Wiley | |
| Book/s | Student Edition | |
| | 2. Operating System-Internals and Design Principles, W.Stallings, 6th Ea | lition, Pearson. |
| | 3. Modern Operating System, Andrew s Tanenbaum, 3rd Edition, PHI | |
| | 4. Operating System A concept-based Approach, 2nd Edition, D.M.Dham | dhere, TMH. |
| 1 | | |
| | | |

SEMESTER 8



| Course Code | CSP402 | | | | | | |
|---------------------|--|-----------------------------|------------|-----|----------|-----|---------------|
| Course Title | Internet of Things | | | | | | |
| Course Outcomes | On the completion of the course the student will be able to: CO 1: Learn and usage of the term "internet of things" in different contexts. CO 2 Understand the key components that make up an IoT system. CO 3: Understand the concepts of Data Acquiring and Business Models for Business Processes CO 4: To learn about Data Collection and IOT cloud-based services | | | | | | |
| Examination Mode | Theory/ P | ractical/ Theory- | + Practica | 1. | | | |
| Assessment Tools | Written Quiz | Assignment/ Project Work | MSE | MTP | ESE | EPR | ABL/PBL |
| Weightage | 10% | 10% | 25% | - | 50% | - | 5% |
| Syllabus | | | | | | | CO Mapping |
| Unit 1 | Introduct | tion (15 Hours) |) | | | | CO1 |
| | An Overview of Internet of things, Internet of Things Technology, Behind Io Ts Sources of the Io Ts, M2M Communication, Examples of IoTs. Design Principles For Connected Devices Internet Connectivity Principles, Internet connectivity | | | | | | |
| | Application Layer Protocols: HTTP, HTTPS, FTP, Telnet. Business Models for Business Processes in the Internet of Things, IoT/M2M systems LAYERS AND designs standardizations, Modified OSI Stack for the IoT/M2M Systems ,ETSI M2M domains and High-level capabilities | | | | | | |
| Unit 2 | , | | 10 Hour | | t | | CO2 |
| | Communic Consolidatesigning Design Princes, V Devices, V Devices, V Devices, V | cted- | | | | | |
| Unit 3 | , | • | (10 H | | | | CO3 |
| | Data Acquiring, Organizing and Analytics in IoT/M2M, Applications /Services /Business Processes, IOT/M2M Data Acquiring and Storage Business Models for Business Processes in the Internet Of Things, Organizing Data, Transactions, Business Processes, Integration and Enterprise Systems. | | | | | | |
| Unit 4 | 6-1110 | | (10 Ho | | | | CO 4 |

| | Data Collection, Storage and Computing Using a Cloud | | | | | | |
|-----------|--|--|--|--|--|--|--|
| | Platform for IoT/M2M Applications/Services, Data | | | | | | |
| | Collection, Storage and Computing Using cloud platform | | | | | | |
| | Everything as a service and Cloud Service Models | | | | | | |
| | IOT cloud-based services using the Xively | | | | | | |
| | (Pachube/COSM), Nimbits and other platforms Sensor, | | | | | | |
| | Participatory Sensing, Actuator, Radio Frequency | | | | | | |
| | Identification, and Wireless, Sensor Network Technology | | | | | | |
| Reference | 1. Rajkamal, Internet of Things: Architecture, Design Principles And | | | | | | |
| Book/s | Applications, McGraw Hill Higher Education, 2017. | | | | | | |
| | 2. A.Bahgya and V.Madisetti, Internet of Things, Univesity Press, 2015 | | | | | | |
| | 3. Adrian McEwen and Hakim Cassimally, Designing the Internet of | | | | | | |
| | Things, Wiley, 2013. | | | | | | |
| | 4. CunoPfister, Getting Started with the Internet of Things, Oreilly, | | | | | | |
| | 2011. | | | | | | |
| | 5. <i>Prof.</i> Satish Jain Shashi Jain, IoT and its applications, bpb | | | | | | |
| | | | | | | | |
| | | | | | | | |



| L | T | P | Credits |
|---|---|---|---------|
| 3 | 0 | 2 | 4 |

| C C - 1 | CCD411 | | | | | 3 | 0 . | | 4 |
|------------------|--|--|--------------|-------------|-----------------|-------|-----|-----|-------|
| Course Code | CSP411 | D | | | | | | | |
| Course Title | On the completion of the course the student will be able to: | | | | | | | | |
| Course Outcomes | | | | | | | | | |
| | | CO 1: Fundamentals of image processing, basic filters and image processing | | | | | | | |
| | operations. | | | | | | | | |
| | CO 2 Image Enhancement operations in Spatial and Frequency domain. | | | | | | | | |
| | CO 3: Color and Morphological Image Processing and applications of image | | | | | | | | |
| | | processing. CO 4: Image Compression and its methods. | | | | | | | |
| | CO 4. IIIIa | ge Compression | and its in | emous. | | | | | |
| Examination | Theory/Pi | ractical/ Theory+ | Practical | <u> </u> | | | | | |
| Mode | Theory/Th | actical/ Theory | · I ractical | L. | | | | | |
| Assessment Tools | Written | Assignment/ | MSE | MTP | ESE | EP | p | ABL | /DRI |
| Assessment 100is | Quiz | Project Work | WISE | IVIII | LSE | | | ADL | /I DL |
| Weightage | 10% | 10% | 25% | _ | 50% | ١. | _ | 5 | 5% |
| Syllabus | 1070 | 1070 | 2370 | _ | 3070 | | | CO | 70 |
| Synabas | | | | | | | | Map | ning |
| Unit 1 | Introduct | ion (15 Hours) | | | | | | CO | |
| 0 222 2 | Fundamental Steps in Image Processing. | | | | | | | | |
| | Element of Visual Perception | | | | | | | | |
| | A simple image model, sampling and quantization | | | | | | | | |
| | | Some Basic Relationships Between Pixel, Image Geometry in | | | | | | | |
| | 2D. | | | | | | | | |
| | . Image Processing Techniques: Basic Intensity | | | | | | | | |
| | Transformation Functions, Image Restoration | | | | | | | | |
| | | Histogram Processing: Histogram Equalization, Histogram | | | | | | | |
| | matching, | Local Histog | ram Proce | essing, Us | sing Histo | ogran | n | | |
| | Statistics | for Image Enh | | | | | | | |
| | Image Su | btraction, Image | Averagin | ıg | | | | | |
| | Filtering: | Smoothing Spat | ial Filters | , Sharpen | ing Spati | al | | | |
| | Filters | | | | | | | | |
| Unit 2 | Introduct | ion to the Fouri | er Trans | formatio | n. (10 H | ours |) | CO | 12 |
| | | ourier Transforn | | | | | on. | | |
| | Image Sm | oothing Using Fr | requency | Domain I | Filters: Id | eal | | | _ |
| | Lowpass F | | | | | | | | |
| | | th low pass filter | | _ | | | _ | | |
| | ' | g Using Frequen | cy Domai | in Filters: | Ideal Hig | ghpa | ss | | |
| | Filters, | | | | | | | | |
| | | th High pass filte | | _ | - | | | | |
| | _ | lasking, Highbo | ost Filteri | ng and H | igh Frequ | ency | 7_ | | |
| | Emphasis | filtering. | | | | | | | |
| | | | | | | | | | |
| TT 1/ 0 | m : • | | | • / | \ TT \ \ | | | ~~ | |
| Unit 3 | Techniqu | es of Color Ima | ige Proce | ssing (10 |) Hours) | | | CC |)3 |

| | Color image signal representation. | |
|------------|---|----------------|
| | Color System Transformations. | |
| | Extension of Processing Techniques to Color Domain | |
| | Morphological Image Processing: Erosion and Dilation, | |
| | Opening and Closing, Hit – or- miss Transformations | |
| | Applications of Image Processing: Picture Data Archival, | |
| | Machine Vision, Medical Image Processing | |
| Unit 4 | Introduction to Image Compression (10 Hours) | CO4 |
| | Coding Redundancy | |
| | Spatial and Temporal Redundancy | |
| | Irrelevant Information | |
| | Measuring Image Information | |
| | Basic Compression Methods: Huffman Coding, LZW | |
| | Coding | |
| | Run Length Coding, Wavelet Coding. | |
| TextBook/s | | |
| Reference | 1. Gonzalez Rafael C. and Woods Richard E., Digital Image I | Droggsing |
| Book/s | New Delhi: Prentice—Hall ofIndia, 2002. | rocessing, |
| DUUK/S | 2. Pratt William K., Digital Image Processing: PIKS Inside(3 | rd ad) Naw |
| | Jersy: John Wiley & Sons, | ra ea. j, rvew |
| | Inc., 2001. | |
| | 3. Bernd Jahne, Digital Image Processing, (5th revised and ex | rtended |
| | edition), Springer, | мениси |
| | 2002 | |
| | 4. AnnaduraiS. and ShanmugalakshmiR., Fundamentals of Di | gital Image |
| | Processing, | guai mage |
| | New Delhi: Pearson Education, 2007 | |
| | 5. Joshi M.A., Digital Image Processing: An Algorithmic App | roach. New |
| | Delhi: Prentice-Hall ofIndia, 2006 | |
| | 6. Sridhar ,Digital Image Processing 2ed, Oxford University | Press. |
| | 7. Rafael C. Gonza Lez, Digotal Image Processing, Fourth E | |
| | 2,500 2 | |
| | 1 | |



| L | T | P | Credits |
|---|---|---|---------|
| 4 | 0 | 0 | 4 |

| Course Code | CSP420 | | | | | | | | |
|---------------------|--|--|-------------|-------------|------------|-------------|------------|--|--|
| Course Title | Cloud Computing | | | | | | | | |
| Course Outcomes | On the con | mpletion of the c | ourse the | student v | vill be ab | ole to: | | | |
| | | CO 1: To understand the basic building blocks and evolution of cloud | | | | | | | |
| | computing as well as aspects of cloud security. | | | | | | | | |
| | CO 2 Able to implement virtualization by creating different types of virtual | | | | | | | | |
| | | machines on physical hosts. CO 3: To understand the advancements in the cloud computing platforms. | | | | | | | |
| | | | | | | | | | |
| | | understand concep | ots of clou | d computii | ng by crea | ating Cloud | d platform | | |
| Ein-di | | al machines | D4: | 1 | | | | | |
| Examination | I neory/ P | ractical/ Theory- | F Practica | ll. | | | | | |
| Mode | Written | A asignment/ | MCE | MTD | ECE | EDD | A DI /DDI | | |
| Assessment Tools | | Assignment/ | MSE | MTP | ESE | EPR | ABL/PBL | | |
| Weightage | Quiz | Project Work | | | | | | | |
| weightage | | | | | | | | | |
| Syllabus | | | | | | | CO | | |
| | | Mapping | | | | | | | |
| Unit 1 | | tion to Cloud Co | | | | | CO 1 | | |
| | | ion to cloud com | | | | | | | |
| | | e Model, Benefit | | tions, Ope | en Challe | enges, | | | |
| | Grid and Utility Computing. | | | | | | | | |
| | | n of Virtualizatio | | | | | | | |
| | | ns, Virtualization | | | | | G . • | | |
| Unit 2 | | chitecture and S | | | | | CO 2 | | |
| | | mputing Architec | | | | • | | | |
| | | Cloud Entities, Cl | loud Clie | nts, Cloud | i Progran | nmıng | | | |
| | Models. Cloud Terminology: Resource Provisioning, Bill | | | | | | | | |
| | | | | | | ovol | | | |
| | Management, Multi-tenancy and Isolation, Service Level | | | | | | | | |
| | Agreement (SLA) and Quality of Service (QoS), Mobile Cloud Computing, Mobile Cloud Service Models. | | | | | | | | |
| Unit 3 | | ecurity and Priv | | | | ırc) | CO 3 | | |
| Cint 5 | | | | | | | CO 3 | | |
| | Cloud Security: Infrastructure Security, Data Security, Identity and Access | | | | | | | | |
| | | nent, Privacy Ma | nagemen | t. Security | v as a Sei | rvice on | | | |
| | Cloud. | , , we j 111u | | ., ~ | , | | | | |
| | | nce and auditing | for cloud | operation | ıs. | | | | |
| | | 6 | | 1 | | | | | |
| | Threats, 1 | risk and requiren | nents land | lscape | | | | | |
| | | data and digital i | | | | | | | |
| | | | | | | | | | |

| | Data sensitivity, location and legal jurisdiction. | |
|------------|--|--------------------|
| | Cloud security approaches and challenges. | |
| Unit 4 | Cloud Simulators (6 Hours) | CO 4 |
| | Introduction to CloudSim simulators, its architecture and | |
| | working | |
| | Cloud computing simulation using GreenCloud. | |
| | Introduction to VMWare Simulator, basics, advantages, using | |
| | VMWare, understanding virtual machines, creating virtual | |
| | machine, cloning virtual machine starting and stopping virtual | |
| | machines. | |
| TextBook/s | | |
| Reference | 1. Barrie Sosinsky, Cloud Computing Bible, Wiley India Pv | t. Ltd, |
| Book/s | ISBN-13: 978-8-12-6529803, New Delhi, India, 2011. | |
| | 2. Dr. Saurabh Kumar, Cloud Computing: Insights Into Ne | w-Era |
| | Infrastructure, Wiley India Pvt.Ltd, ISBN-13: 978-8-12-6528837 | ⁷ , New |
| | Delhi, India, 2011 | |
| | 3. Fern Halper, Hurwitz, Robin Bloor, Marcia Kaufman, C | loud |
| | Computing For Dummies, Wiley India Pvt. Ltd, ISBN-13: 978-0 | -47- |
| | 0597422, New Delhi, India, 2011. | |
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